PREPARED BY: DATE:			SPEC. No.	SA-88049D
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M. Mitsui March 28, 1996			PAGE	15 Pages
APPROVED BY: DATE: Typs whave Min 1.22 1996	ELECTRONIC COMPONE GROUP SHARP CORPOR		REPRESEN	TATIVE DIVISION
J Jushmana 111 J. J.	SPECIFICATION	ON	OPTO-ELEC	TRONIC DEVICES DIV.
DEVIC	CE SPECIFICATION FOR			
P. MODE	HOTOCOUPLER	Business dea	ling name	
	PC900V	PC	2900VY	
	clude materials protected under co ause anyone to reproduce them wi			on ("Sharp").
in these specification sheets for any damage resulting fro	ease observe the absolute maximur , as well as the precautions mentio m use of the product which does n d in these specification sheets, and	ned below. ot comply w	Sharp assumes ith the absolute	no responsibility maximum ratings
(Precautions)				
(1) This product is designed for use in the following application areas: OA equipment · Audio visual equipment · Home appliances				
1	tion equipment (Terminal) • Meas			
<u> </u>	· Tooling machines · Computers			
	roduct in the above application are e sure to observe the precautions (
the safety design of and safety when th	res, such as fail-safe design and re the overall system and equipment is product is used for equipment w nd precision, such as ;	, should be	taken to ensure	reliability
	control and safety equipment (aircr	aft, train, a	itomobile etc.)	7
· · · · · · · · · · · · · · · · · · ·	· Gas leakage sensor breakers ·			ment
(3) Please do not use 1	this product for equipment which r	equire extre	mely high relial	oility
_ '	on and precision, such as ;		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
	t · Telecommunication equipment ontrol equipment · Medical equip		imes)	
	consult with a Sharp sales represention of the above three paragraphs		ere are any que	stions
3. Please contact and consult w	ith a Sharp sales representative fo	r any questi	ons about this	product.
	Г	ATE		
CUSTOMER'S APPROVAL		RESENTEI Y) J.	\mathcal{M}
DATE		Depart	umura, nent General	Manager of
BY		Opto-E ELECO	ering Dept.,II lectronic Devid M Group CORPORATIC	

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1. Application

This specification applies to the outline and characteristics of photocopier Model No. PC900V.

2. Outline

Refer to the attached drawing No. CY5440K02.

3. Ratings and characteristics

Refer to the attached sheet, page 3 to 5.

4. Reliability

Refer to the attached sheet, page 8.

5. Incoming inspection

Refer to the attached sheet, page 9.

6. Supplement

- 6.1 Isolation voltage shall be measured in the following method.
- (1) Short among pins 1 to 3 on the primary side and among pins 4 to 6 on the secondary side.
- (2) The dielectric withstand tester with zero-cross circuit shall be used.
- (3) The wave form of applied voltage shall be a sine wave.
 (It is recommended that the isolation voltage be measured in insulation oil.)

6.2 Business dealing name

(" \bigcirc " mark indicates business dealing name of ordered product)

Product	Business dealing name	Remarks
0	PC900V	
	PC900VY	Applied to products as a option (Attached sheets -2-1 to 2-4.)

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6.3 This Model is approved by UL.

Approved Model No.: PC900V

UL file No.: E64380

6.4 Theory of operation

- (1) When the forward current of above the " $H \rightarrow L$ " threshold input current (I_{FHL}) is applied to the input side, the output will go "Low level".
- (2) When the forward current on the input side goes below the "L \rightarrow H" threshold input current (I_{FLH}) the output will go "High level".
- 6.5 This product is not designed against irradiation.

This product is assembled with electrical input and output.

This product incorporates non-coherent light emitting diode.

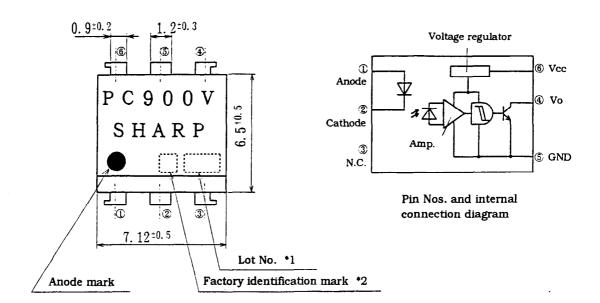
7. Notes

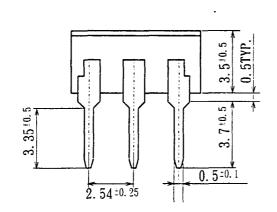
Refer to the attached sheet-1-1, 2.

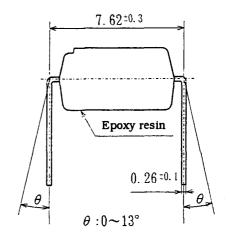
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- *1) 2-digit number shall be marked according to DIN standard.
- *2) Factory identification mark shall be or shall not be marked.

`	UNIT: 1/1 mm
Name	PC900V Outline Dimensions (Business dealing name : PC900V)
Drawing No.	CY5440K02

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3. Ratings and characteristics

3.1 Absolute maximum ratings

Ta=25℃

Parameter		Symbol	Rating	Unit
	*1 Forward current	I _F	50	mA
	*2 Peak forward current	I _{FM}	1	A
Input	Reverse voltage	V_R	6	V
	Power dissipation	P	70	mW
	Supply voltage	Vcc	16	V
0	High level output voltage	V _{OH}	16	v
Output	Low level output current	I _{OL}	50	mA
	*1 Power dissipation	Po	150	mW
	*1 Total power dissipation	Ptot	170	mW
	*3 Isolation voltage	Viso	5.0	kVrms
	Operating temperature	Topr	-25 to +85	င
	*4 Storage temperature	Tstg	-40 to 125	င
	*4 Soldering temperature	Tsol	260	င

^{*1} The derating factors of absolute maximum ratings due to ambient temperature are shown in Fig. 1, 2, 3.

^{*2} Pulse width \leq 100 μ s, Dutyratio: 0.001

^{*3} AC for 1 min, 40 to 60%RH

^{*4} For 10 s

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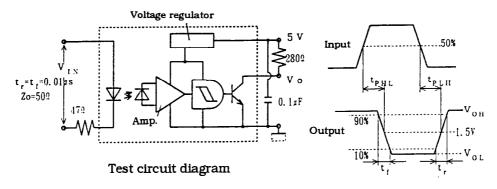
3.2 Electro-optical characteristics

(Unspecified : Ta=0 to 70° C)

(Onspeciment : Ta=0 to 70 0)							
	Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Conditions
Forward voltage		V	-	1.1	1.4	V	I _F =4mA
	orward voitage	V _F	0.7	1.0	-	·	I _F =0.3mA
F	Reverse current	I _R	-	-	10	μΑ	Ta=25℃, V _R =3V
7	Cerminal capacitance	Ct	-	30	250	pF	Ta=25°C, V=0 f=1kHz
		Vcc	3	•	15	V	
I	ow level output voltage	V _{OL}	-	0.2	0.4	V	I _{OL} =16mA, Vcc=5V I _F =4mA
ŀ	ligh level output voltage	I _{ОН}	-	-	100	μA	Vcc=Vo=15V I _F =0mA
L	ow level supply current	I _{CCL}	-	2.5	5.0	mA	Vcc=5V, I _F =4mA
ŀ	ligh level supply current	I _{CCH}	-	1.0	5.0	mA	Vcc=5V, I _F =0mA
	"H→L" threshold input	I _{FHL}	-	1.1	2.0	mA	Ta=25℃, Vcc=5V R _L =280 Ω
current 1			-	-	4.0		Vcc=5V, R_L =280 Ω
["	L→H" threshold input	I_{FLH}	0.4	0.8	,	mA	Ta=25℃,Vcc=5V R _L =280 Ω
С	urrent *2		0.3	•	-		Vcc=5V, R _L =280 Ω
I	lysteresis *3	I _{FLH} /I _{FHL}	0.5	0.7	0.9		Vcc=5V, R_L =280 Ω
Is	solation resistance	Riso	5×10 ¹⁰	1011	-	Ω	Ta=25℃, DC500V 40 to 60%RH
me	"H→L" propagation time	t _{PHL}	-	1	3		Та=25℃
ıse ti	"L→H" propagation time	t _{PLH}	-	2	6	μs	Vcc=5V, I _F =4mA
sspor	Fall time	tf	-	0.05	0.5	,,,	$R_L=280 \Omega$
Re	Rise time	tr	_	0.1	0.5		- L =
	F T T C T T C C T T T T T T T T T T T T	Forward voltage Reverse current Terminal capacitance Operating supply voltage range Low level output voltage High level output voltage Low level supply current High level supply current "H→L" threshold input current *1 "L→H" threshold input current *2 Hysteresis *3 Isolation resistance "H→L" propagation time "L→H" propagation time Fall time	Forward voltage V_F Reverse current I_R Terminal capacitance Ct Operating supply voltage range V_{CL} Low level output voltage V_{CL} High level output voltage I_{OH} Low level supply current I_{CCL} High level supply current I_{CCH} "H \rightarrow L" threshold input current *1 "L \rightarrow H" threshold input I_{FHL} "L \rightarrow H" threshold input I_{FLH} current *2 Hysteresis *3 I_{FLH}/I_{FHL} Isolation resistance I_{CH} "H \rightarrow L" propagation time I_{CH} "L \rightarrow H" propagation time I_{CH} Fall time I_{CH}	Forward voltage $V_{F} = \frac{-}{0.7}$ Reverse current $I_{R} = -$ Terminal capacitance $Ct = -$ Operating supply voltage range $V_{OL} = -$ High level output voltage $V_{OL} = -$ High level supply current $I_{CCL} = -$ High level supply current $I_{CCH} = -$ $"H \rightarrow L" \text{ threshold input current *1}$ $-$ $"L \rightarrow H" \text{ threshold input current *2}$ $-$ $T_{FLH} = -$ $-$ $V_{CL} = -$ $-$ $V_{CL} = -$ $-$ $-$ $-$ $-$ $-$ $-$ $-$ $-$ $-$	Forward voltage $V_{F} = \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

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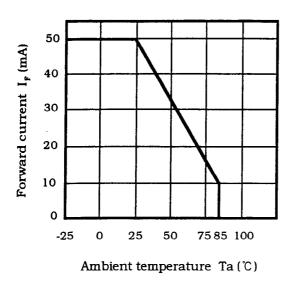
- *1 I_{FHL} represents forward current when output goes from "H" to "L".
- *2 I_{FLH} represents forward current when output goes from "L" to "H".
- *3 Hysteresis : I_{FLH}/I_{FHL}
- *4 Test circuit for response time shall be shown below.



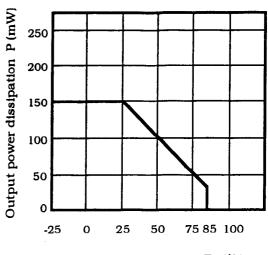
Timing chart

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(Fig. 1) Forward current vs. ambient temperature

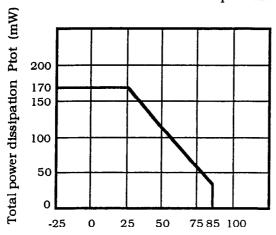


(Fig. 2) Output power dissipation vs. ambient temperature



Ambient temperature Ta (℃)

(Fig. 3) Total power dissipation vs. ambient temperature



Ambient temperature Ta(℃)

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4. Reliability

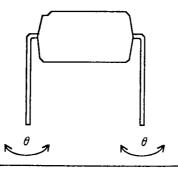
The reliability of products shall be satisfied with items listed below.

Confidence level: 90% LTPD: 10%/20%

Test Items	Test Conditions	Failure Judgement Criteria	Samples (n) Defective(C)
Solderability *2	230℃, 5 s		n=11, C=0
Soldering heat	260℃, 10 s	$V_F>U\times1.2$	n=11, C=0
Terminal strength (Tension)	Weight : 5N 5 s/each terminal	I _R >U×2	n=11, C=0
Terminal strength (Bending) *3	Weight: 2.5N 2 times/each terminal	$V_{OL} > U \times 1.2$ $I_{OH} > U \times 1.2$	n=11, C=0
Mechanical shock	15000m/s ² , 0.5ms 3 times/ \pm X, \pm Y, \pm Z direction	I _{CCL} >U×1.2	n=11, C=0
Variable frequency vibration	100 to 2000 to 100Hz/4min 200m/s ² 4 times/ X, Y, Z direction	$I_{CCH} > U \times 1.2$ $I_{FHL} > U \times 1.3$	n=11, C=0
Temperature cycling	1 cycle -40°C to +125°C (30min) (30min) 20 cycles test	$I_{FLH} < L \times 0.8$ $I_{FLH} / I_{FHL} \neq L \times 0.8$ $\sim U \times 1.2$	n=22,C=0
High temp. and high humidity storage	+60°C, 90%RH, 1000h	U : Upper	n=22,C=0
High temp. storage	+125°C, 1000h	specification limit	n=22,C=0
Low temp. storage	-40°, 1000h	L : Lower specification	n=22,C=0
Operation life	I _F =10mA, Vcc=15V I _{OL} =16mA, Ta=25°C, 1000h	limit	n=22,C=0

^{*1} Test method, conforms to JIS C 7021.

^{*3} Terminal bending direction is shown below.



^{*2} Solder shall adhere at the area of 95% or more of immersed portion of lead and pin hole or other holes shall not be concentrated on one portion.

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5. Incoming inspection

- 5.1 Inspection items
- (1) Electrical characteristics

$$\mathbf{V_{F}},\,\mathbf{I_{R}},\,\mathbf{V_{OL}},\,\mathbf{I_{OH}},\,\mathbf{I_{CCL}},\,\mathbf{I_{CCH}},\,\mathbf{I_{FHL}},\,\mathbf{I_{FLH}},\,\mathbf{Riso},\,\mathbf{Viso}$$

- (2) Appearance
- 5.2 Sampling method and Inspection level

A single sampling plan, normal inspection level II based on ISO 2859 is applied. The AQL according to the inspection items are shown below.

Defect	Inspection item	AQL (%)
Major defect	Electrical characteristics Unreadable marking	0.1
Minor defect	Appearance defect except the above mentioned.	0.4

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PC900V Attach sheet-1-1

Precautions for Photocouplers

1. For cleaning

(1) Solvent cleaning : Solvent temperature 45°C or less Immersion for 3 min or less

(2) Ultrasonic cleaning: The affect to device by ultrasonic cleaning is different

by cleaning bath size, ultrasonic power

output, cleaning time, PWB size or device mounting condition etc. Please test it in actual using condition and confirm that doesn't occur any defect before starting

the ultrasonic cleaning.

(3) Applicable solvent : Ethyl alcohol, Methyl alcohol

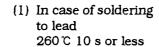
Freon TE · TF, Diflon-solvent S3-E

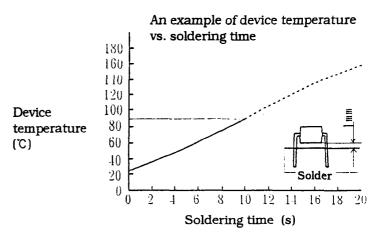
Please refrain form using Chloro Fluoro Carbon type solvent to clean devise as much as possible since it is internationally restricted to protect the ozonosphere. Before you use alternative solvent you are requested to confirm that it does not attack package resin.

- 2. Please use the same as normal integration circuit about static electricity in order that this device is OPIC photocopier.
- 3. In order to stabilize power supply line, we recommend to connect a by-pass capacitor of 0.01 μ F or more between Vcc and GND near the device.
- 4. The LED used in the Photocoupler generally decreases the light emission power by operation. In case of long operation time, please design the circuit with considering the decreases of the light emission power of the LED. (50%/5years) Please decide the input current which become 2 times of MAX. I_{FHL} .

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PC900V		Attach sheet-1-2

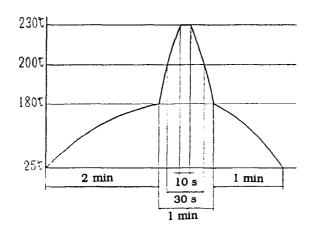
5. Precautions for Soldering Photocouplers





(2) If solder reflow:

It is recommended that only one soldering be done at the temperature and the time within the temperature profile as shown in the figure.



(3) Other precautions

An infrared lamp used to heat up for soldering may cause a localized temperature rise in the resin. So keep the package temperature within that specified in Item (2). Also avoid immersing the resin part in the solder.

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PC900VY (Option)		ttach heets-2-1

1. This specification shall be applied to photocopier, Model No. PC900V as an option.

2. Applicable Models (Business dealing name)

PC900VY

3. The relevant models are the models Approved by TÜV Rheinland Japan according to DIN VDE0884/08.87.

Approved Model No.: PC900V

TÜV approved No.: R9151577

(According to the specification DIN VDE0884/08.87)

• Operating isolation voltage U_{IORM} : 710V (Peak)

• Transient voltage $U_{TR}:6000V$ (Peak)

• Pollution: 2 (According to VDE0110/01.89)

• Clearances distance (Between input and output): 6mm (MIN.)

• Creepage distance (Between input and output): 6mm (MIN.)

• Isolation thickness between input and output: 0.15mm (MIN.)

• Tracking-proof: CTI 225 (Material group IIIa: VDE0110/01.89)

Safety limit values

Current (Isi): 120mA (Diode side)

Power (Psi): 240mW (Phototransistor side)

Temperature (Tsi): 150℃

In order to keep safety electric isolation of photocopier, please set the protective circuit to keep within safety limit values when the actual application equipment troubled.

• Indication of TÜV approval prints "\$\sum_0884" on sleeve package.

4. Outline

Refer to the attached drawing No. CY5164K02.

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PC900VY (Option)		Attach sheets-2-2

5. Isolation specification according to VDE 0884

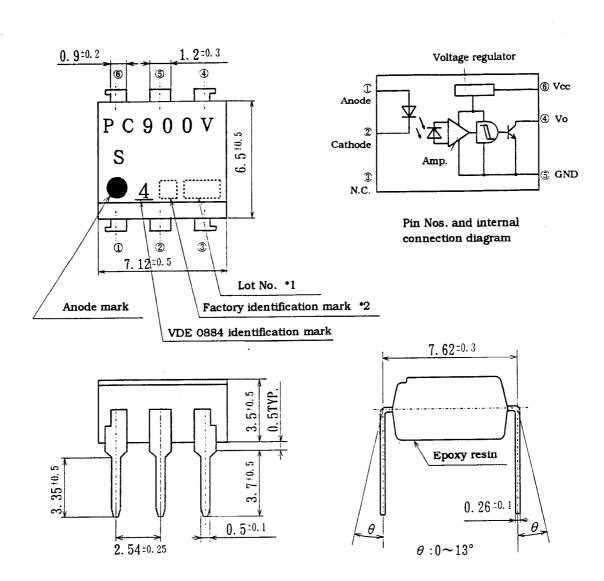
	F	Parameter	Symbol	Conditions	Rating	Unit	Remark
CI te:		vironmental	•	DIN IEC68	25/85/21	-	
Po	ollution		-	DIN VDE0110	2	-	
	aximum o	operating oltage	U _{IORM}	-	710	V _{PEAK}	
Partial discharge test voltage (Between input and output)						Refer to the Dia- gram 1, 2	
-		Diagram 1	Upr	tp=60 s, qc<5pC	852	V _{PEAK}	gram 1, 2
		Diagram 2		tp=1 s, qc<5pC	1136	$V_{ ext{PEAK}}$:
Ma	aximum (over-voltage	U _{INITIAL}	t _{INI} =10 s	6000	V_{PEAK}	:
Safety maximum ratings							
	1) Case	e temperature	· Tsi	I _F =0, Pc=0	150	င	Refer to
	2) Inpu	ıt current	Isi	Pc=0	120	mA	the Fig.
	(Out	tric power put or Total power sipation)	Psi	-	240	mW	0, 7
Isolation resistance (Test voltage between input and output ; DC500V)			Ta=Tsi	MIN. 10 ⁹			
		R _{ISO}	Ta=Topr (MAX.) Ta=25℃	MIN. 10 ¹¹	Ω		

6. Precautions in performing isolation test

- $6.1\,$ Partial discharge test methods shall be the ones according to the specifications of VDE 0884/08.87
- 6.2 Please don't carry out isolation test (Viso) over $U_{INITIAL}$. This product deteriorates isolation characteristics by partial discharge due to applying high voltage (ex. $U_{INITIAL}$). And there is possibility that this product occurs partial discharge in operating isolation voltage. (U_{IORM}).

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PC900VY (Option) Attach sheets-2-3

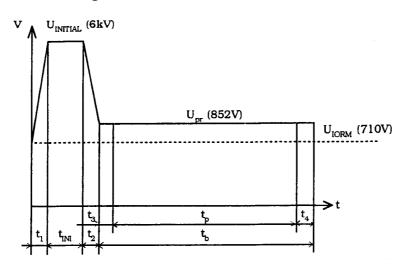


- *1) 2-digit number shall be marked according to DIN standard.
- *2) Factory identification mark shall be or shall not be marked.

UNIT: 1/1 mm		
Name	PC900V Outline Dimensions (Business dealing name : PC900VY)	
Drawing No.	CY5164K02	

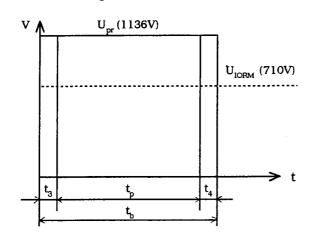
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PC900VY (Option)		Attach sheets-2-4

Method of Diagram 1: Breakdown test (Apply to type test and sampling test)



 $\begin{array}{lll} t_1, \, t_2 & = 1 \,\, \text{to} \,\, 10 \,\, \text{s} \\ t_3, \, t_4 & = 1 \,\, \text{s} \\ t_p \,\, (\text{Partial discharge measuring time}) & = 60 \,\, \text{s} \\ t_b & = 62 \,\, \text{s} \\ t_{\text{INI}} & = 10 \,\, \text{s} \end{array}$

Method of Diagram 2: Non breakdown test (Apply to all device test)



t₃, t₄ =0.1 s t_p (Partial discharge =1 s measuring time) t_b =1.2 s

Fig. 6 Safety maximum power dissipation vs. ambient temperature (When failed)

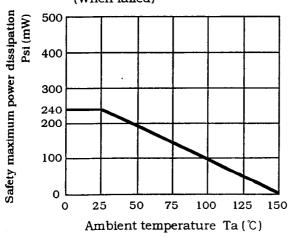


Fig. 7 Safety maximum forward current vs. ambient temperature (When failed)

