Switching Diode

Features

- SOD-123 Surface Mount Package
- High Breakdown Voltage
- Fast Speed Switching Time
- Pb-Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	V _R	100	Vdc
Peak Forward Current	l _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit	
Total Device Dissipation FR-5 Board (Note 1) $T_{\Delta} = 25^{\circ}C$	P_{D}	425	mW	
Derate above 25°C		3.4	mW/°C	
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	290	°C/W	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \text{ oz Cu}, 1.0 \text{ in}^2 \text{ pad}$



ON Semiconductor®

http://onsemi.com





SOD-123 CASE 425 STYLE 1

MARKING DIAGRAM



51 = Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

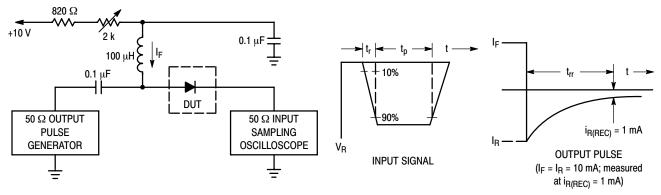
ORDERING INFORMATION

Device	Package	Shipping [†]
MMSD4148T1	SOD-123	3000 / Tape & Reel
MMSD4148T1G	SOD-123 (Pb-Free)	3000 / Tape & Reel
MMSD4148T3	SOD-123	10,000 / Tape & Reel
MMSD4148T3G	SOD-123 (Pb-Free)	10,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I _{BR} = 100 μAdc)	V _(BR)	100	-	Vdc
Reverse Voltage Leakage Current (V _R = 20 Vdc) (V _R = 75 Vdc)	I _R	- -	25 5.0	nAdc μAdc
Forward Voltage (I _F = 10 mAdc)	V _F	-	1000	mVdc
Diode Capacitance (V _R = 0 Vdc, f = 1.0 MHz)	C _D	-	4.0	pF
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc})$ (Figure 1)	t _{rr}	_	4.0	ns



- 1. A 2.0 $k\Omega$ variable resistor adjusted for a Forward Current (IF) of 10 mA.
- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

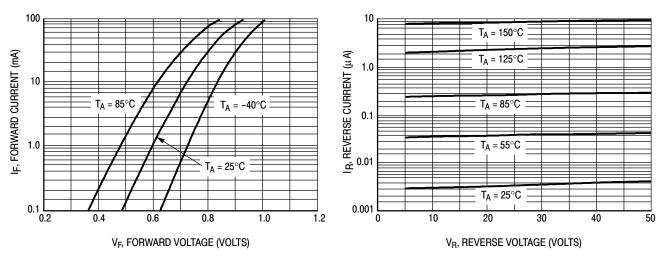


Figure 2. Forward Voltage

Figure 3. Leakage Current

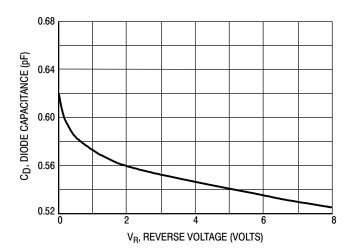
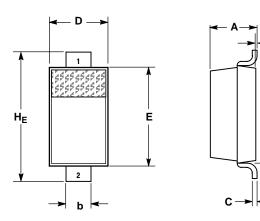


Figure 4. Capacitance

PACKAGE DIMENSIONS

SOD-123 CASE 425-04 ISSUE E

A1



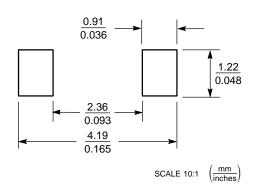
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
 Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.94	1.17	1.35	0.037	0.046	0.053
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.51	0.61	0.71	0.020	0.024	0.028
С			0.15			0.006
D	1.40	1.60	1.80	0.055	0.063	0.071
Е	2.54	2.69	2.84	0.100	0.106	0.112
HE	3.56	3.68	3.86	0.140	0.145	0.152
L	0.25			0.010		

STYLE 1: PIN 1. CATHODE 2. ANODE

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and the registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any iability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082–1312 USA Phone: 480–829–7710 or 800–344–3860 Toll Free USA/Canada Fax: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800–282–9855 Toll Free LISA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2–9–1 Kamimeguro, Meguro–ku, Tokyo, Japan 153–0051 Phone: 81–3–5773–3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.