

FEATURES AND SPECIFICATIONS

Features and Benefits

- Accepts JEDEC MO-161 modules for 100% industry compatibility
- Dual ejector latches for easy module removal
- Latches include feature to minimize micro motion
- Palladium Nickel with Gold flash plating for improved reliability
- Added contact wipe for improved reliability
- Optional voltage and function keying

Reference Information

Product Specification: PS-71243-9999
 UL File No.: E29179
 CSA File No.: LR-19980A-366
 Packaging: Tray
 Mates With: JEDEC MO-161 modules
 Designed In: Inches

Electrical

Voltage: 100V
 Current: 1.0A
 Contact Resistance: 30mΩ max.
 Dielectric Withstanding Voltage: 500V AC
 Insulation Resistance: 1000 MΩ min.

Mechanical

Mating Force: 1.6N (0.35lb) per pair
 Durability: 25 cycles min.

Physical

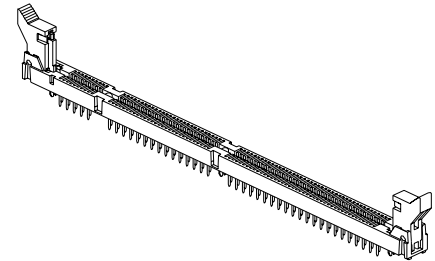
Housing and Latch: High temperature resistant thermoplastic, UL 94V-0
 Contact: Phosphor Bronze
 Forklock: Stainless Steel
 Plating: Selective Palladium Nickel with Gold flash with Tin/Lead on solder tails
 Operating Temperature: -40 to +85°C



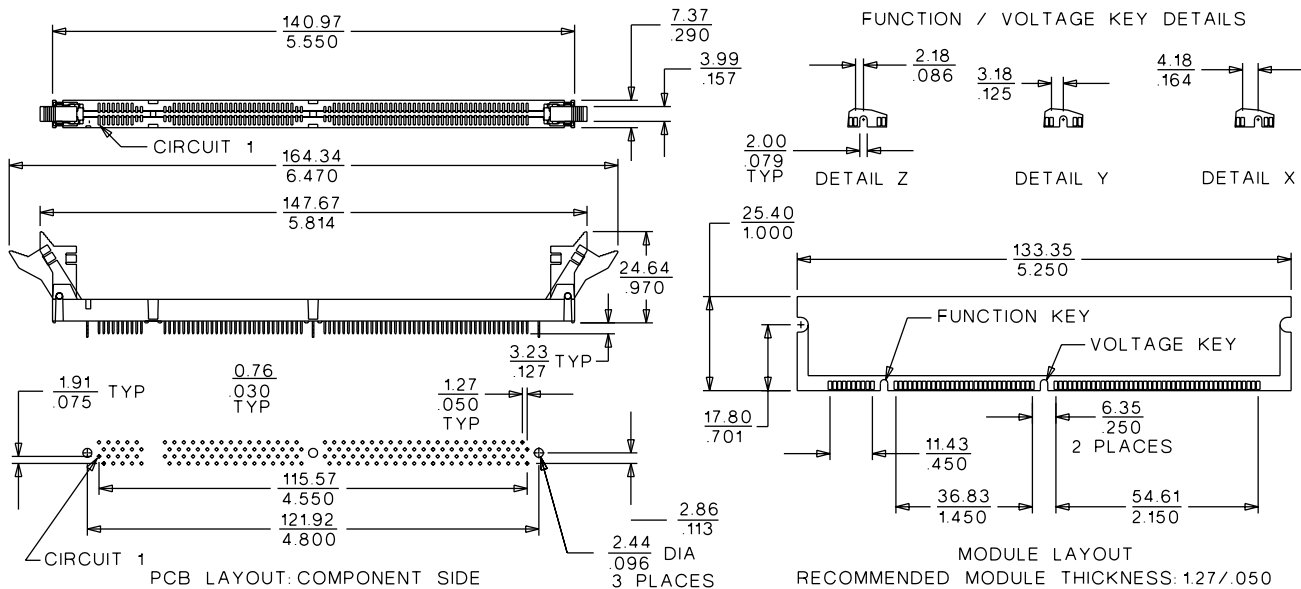
1.27mm (.050") Pitch DIMM 8-Byte DRAM Socket

71736

Vertical, Multi-Key Forklock Version



CATALOG DRAWING (FOR REFERENCE ONLY)



ORDERING INFORMATION

Circuits	Order No.	Function Key	Voltage Key	Description
168	71736-0001	Detail Y	Detail Y	DRAM 3.3V
	71736-0002	Detail Y	Detail Z	DRAM 5.0V
	71736-0006	Detail Z	Detail Y	SDRAM 3.3V
	71736-0007	Detail Z	Detail Z	SDRAM 5.0V
	71736-0011	Detail X	Detail Y	Unbuffered 3.3V
	71736-0005	Detail X	Detail Z	Unbuffered 5.0V