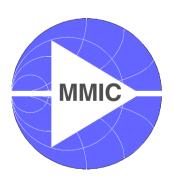
BGA614

Silicon Germanium Broadband MMIC Amplifier



Secure Mobile Solutions
Silicon Discretes



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BGA614
Data sheet
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Preliminary status removed

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Silicon Germanium Broadband MMIC Amplifier

BGA614

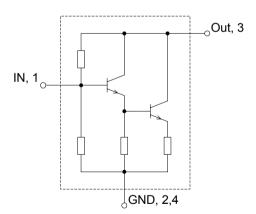
Features

- Cascadable 50Ω-gain block
- 3 dB-bandwidth: DC to 2.4 GHz with 18.5 dB typical gain at 1.0 GHz
- Compression point P_{-1dB} = 12 dBm at 2.0 GHz
- Noise figure $F_{50\Omega} = 2.30 \text{ dB}$ at 2.0 GHz
- Absolute stable
- 70 GHz f_T Silicon Germanium technology

4 2 1 VPS05605

Applications

- Driver amplifier for GSM/PCS/CDMA/UMTS
- · Broadband amplifier for SAT-TV & LNBs
- · Broadband amplifier for CATV



Description

The BGA614 is a broadband matched, general purpose MMIC amplifier in a Darlington configuration. It is optimized for a typical supply current of 40mA.

The BGA614 is based on Infineon Technologies' B7HF Silicon Germanium technology.

ESD: Electrostatic discharge sensitive device, observe handling precaution!

Гуре Package		Marking	Chip	
BGA614	SOT343	BOs	T0565	



Maximum Ratings

Parameter	Symbol	Value	Unit	
Device voltage	V_{D}	3	V	
Device current	I _D	80	mA	
Current into pin In	I _{In}	0.7	mA	
Input power 1)	P _{IN}	10	dBm	
Total power dissipation, T _S < 102°C ²⁾	P _{tot}	240	mW	
Junction temperature	T _j	150	°C	
Ambient temperature range	T _A	-65 +150	°C	
Storage temperature range	T _{STG}	-65 +150	°C	
Thermal resistance: junction-soldering point	R _{th JS}	200	K/W	

Notes:

All Voltages refer to GND-Node

Electrical Characteristics at T_A =25°C (measured in test circuit specified in fig. 1) V_{CC} =5V, R_{Bias} =62 Ω , Frequency=2GHz, unless otherwise specified

Parameter	Symbol	min.	typ.	max.	Unit
Insertion power gain	$ S_{21} ^2$				dB
f = 0.1GHz		-	19.5	-	
f = 1.0GHz		-	18.5	-	
f = 2.0GHz		-	17.0	-	
Noise Figure ($Z_S=50\Omega$)	$F_{50\Omega}$				dB
f = 0.1GHz		-	1.95	-	
f = 1.0GHz		-	2.20	-	
f = 2.0GHz		-	2.30	-	
Output Power at 1dB Gain Compression	P _{-1dB}	-	12	-	dBm
Output Third Order Intercept Point	OIP ₃	-	25	-	dBm
Input Return Loss	RL _{In}	-	19	-	dB
Output Return Loss	RL _{Out}	-	24	-	dB
Total Device Current	I _D	-	40	-	mA

 $^{^{\}rm 1)}$ Valid for Z $_{\rm S}$ =Z $_{\rm L}$ =50 $\!\Omega$, V $_{\rm CC}$ =5V, R $_{\rm Bias}$ =62 $\!\Omega$

²⁾ T_s is measured on the ground lead at the soldering point



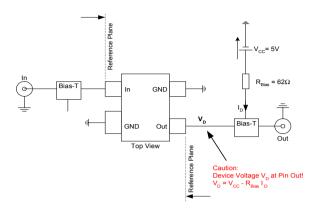


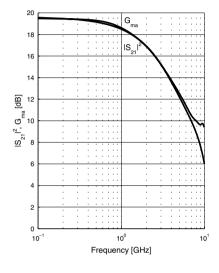
Fig.1: Test Circuit for Electrical Characteristics and S-Parameters

S-Parameter V_{CC} =5V, R_{Bias} =62 Ω (see Electrical Characteristics for conditions)

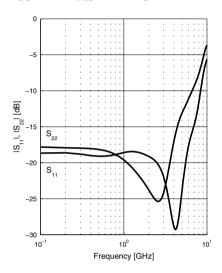
Frequency	S11	S11	S21	S21	S12	S12	S22	S22
[GHz]	Mag	Ang	Mag	Ang	Mag	Ang	Mag	Ang
0.1	0.1245	5.9	9.3122	177.5	0.0840	0.0	0.1288	-0.9
0.2	0.0854	4.6	9.3767	172.8	0.0825	1.5	0.1266	-4.8
0.4	0.1133	11.1	9.1886	165.1	0.0832	2.7	0.1268	-10.0
0.6	0.1115	7.8	9.0552	157.9	0.0837	4.7	0.1220	-16.9
0.8	0.1114	8.5	8.7953	150.8	0.0834	6.6	0.1146	-23.1
1.0	0.1205	9.8	8.5065	144.1	0.0848	8.4	0.1049	-30.4
1.2	0.1165	8.9	8.0863	137.8	0.0857	9.9	0.0948	-37.5
1.4	0.1163	8.4	7.8100	131.1	0.0883	11.4	0.0869	-45.4
1.6	0.1159	6.7	7.4972	125.6	0.0899	13.0	0.0779	-54.7
1.8	0.1164	5.7	7.2744	120.0	0.0923	13.7	0.0706	-65.1
2.0	0.1099	1.0	6.9831	114.8	0.0944	15.1	0.0642	-75.7
3.0	0.0775	-5.3	5.7650	91.5	0.1114	17.9	0.0623	-159.0
4.0	0.0358	31.2	4.7962	71.7	0.1316	17.2	0.1391	163.7
5.0	0.0719	116.9	4.0808	53.3	0.1541	13.3	0.2209	144.4
6.0	0.1365	123.3	3.5461	36.1	0.1759	7.6	0.2793	126.3
7.0	0.1807	111.4	3.0857	20.8	0.1971	1.0	0.3398	113.0
8.0	0.2628	101.8	2.7951	4.4	0.2197	-7.7	0.4199	103.4



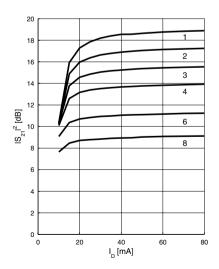
Power Gain
$$|S_{21}|^2$$
, $G_{ma} = f(f)$
 $V_{CC} = 5V$, $R_{Bias} = 62\Omega$, $I_{C} = 40mA$



$$\begin{aligned} & \textbf{Matching} \ |S_{11}|, \ |S_{22}| = f(f) \\ & V_{CC} = 5V, \ R_{Bias} = 62\Omega, \ I_{C} = 40 \text{mA} \end{aligned}$$

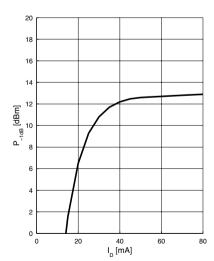


Power Gain $|S_{21}| = f(I_D)$ f = parameter in GHz



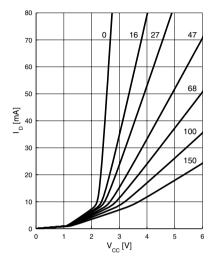
Output Compression Point

$$\boldsymbol{P}_{-1dB} = \boldsymbol{f}(\boldsymbol{I}_{D}), \, \boldsymbol{f} = 2\boldsymbol{G}\boldsymbol{H}\boldsymbol{z}$$

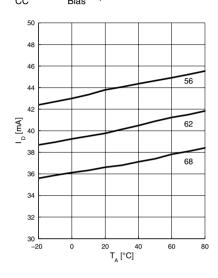




$\begin{array}{l} \textbf{Device Current I}_{D} = \text{f(V}_{CC}) \\ \text{R}_{Bias} = \text{parameter in } \Omega \\ \end{array}$



Device Current I $_{D}$ = f(T $_{A}$) V $_{CC}$ = 5V, R_{Bias} = parameter in Ω



Noise figure F = f(f)

0 L

0.5

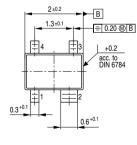
$$V_{CC} = 5V$$
, $R_{Bias} = 62\Omega$, $Z_{S} = 50\Omega$
 $T_{A} = parameter in °C$
 $\frac{3}{2.5}$
 $\frac{+80°C}{-20°C}$
 $\frac{60}{1.5}$
 $\frac{1}{1.5}$

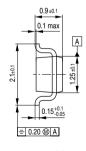
1.5

Frequency [GHz]

2.5

Package Outline





GPS05605

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Datasheets for electronics components.