

Example:

U L 62 H 256 A S K 35

ZMD Product

Power Supply

Blank = 4,5 ... 5,5 V
 L = Low Voltage: 3,0 ... 3,6 V
 2,5 ... 3,6 V
 2,3 ... 2,7 V

Memory Type

62 = SRAM

SRAM-Type

Blank ≥ 70ns (Slow)
 H ≤ 55ns (Fast)

Density

64 = 8K × 8 bit
 256 = 32K × 8 bit
 1708 = 128K × 8 bit
 1616 = 64K × 16 bit

Design Version (A, B, ...) /

Power Supply (for UL62...)

A = 3,0 ... 3,6 V or 2,5 ... 3,6 V
 B = 2,3 ... 2,7 V

Leadfree Option

blank = Standard Package
 G1 = Leadfree Green Package

Supply Curr. (only 64K/256K SRAM)

blank = Standard
 L = Low Power
 LL = Very Low Power

Access Time

15 = 15 ns	55 = 55 ns
20 = 20 ns	07 = 70 ns
35 = 35 ns	10 = 100 ns
45 = 45 ns	

Operating Temp. Range

C = 0 to +70°C
 K = -40 to +85°C
 A = -40 to +125°C

Package

D = PDIP	S/S1/S2 = SOP
K = BGA	T/T1 = TSOP
P = PLCC	