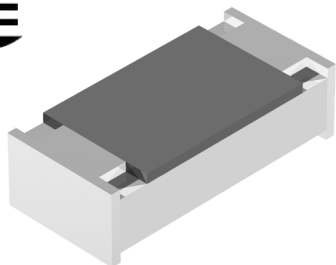


Professional Flat Chip Resistors



MCS 0402, MCT 0603 and MCU 0805 Professional Thin Film Flat Chip Resistors are the perfect choice for most fields of modern professional electronics where reliability and stability is of major concern. Typical applications include telecommunication, medical equipment and high-end computer and audio/video electronics.

FEATURES

- Approved according to EN 140401-801
- Advanced thin film technology
- Advanced dissipation rating: 100 mW for 0603
- Excellent overall stability: Class 0.5
- Green product, supports lead-free soldering.

APPLICATIONS

- Automotive
- Telecommunication
- Medical equipment
- Industrial equipment.

METRIC SIZE

INCH:	0402	0603	0805
METRIC:	RR 1005M	RR 1608M	RR 2012M

TECHNICAL SPECIFICATIONS

DESCRIPTION	MCS 0402		MCT 0603		MCU 0805	
Metric size	RR 1005M		RR 1608M		RR 2012M	
Resistance range	10 Ω to 4.99 MΩ		1 Ω to 10 MΩ		10 Ω to 1.5 MΩ	
Resistance tolerance	± 1 %; ± 0.5 %				± 0.5 %	
Temperature coefficient	± 50 ppm/K; ± 25 ppm/K					
Operation mode	standard	power	standard	power	standard	power
Climatic category (LCT/UCT/days)	55/125/56	55/155/56	55/125/56	55/155/56	55/125/56	55/155/56
Rated dissipation, $P_{70}^{(1)}$	0.063 W	0.1 W	0.1 W	0.125 W	0.125 W	0.2 W
Operating voltage, $U_{\max}$ AC/DC	50 V		75 V		150 V	
Film temperature	125 °C	155 °C	125 °C	155 °C	125 °C	155 °C
Max. resistance change at P_{70} for resistance range, $\Delta R/R$ max., after:	10 Ω to 4.99 MΩ		1 Ω to 10 MΩ		10 Ω to 1.5 MΩ	
1 000 h	≤ 0.25 %	≤ 0.5 %	≤ 0.25 %	≤ 0.5 %	≤ 0.25 %	≤ 0.5 %
8 000 h	≤ 0.5 %	≤ 1.0 %	≤ 0.5 %	≤ 1.0 %	≤ 0.5 %	≤ 1.0 %
225 000 h	≤ 1.5 %		≤ 1.5 %		≤ 1.5 %	
Specified lifetime	225 000 h	8 000 h	225 000 h	8 000 h	225 000 h	8 000 h
Insulation voltage:						
1 minute; U_{ins}	75 V		100 V		200 V	
continuous	75 V		75 V		75 V	
Failure rate	≤ 2 × 10 ⁻⁹ /h		≤ 2 × 10 ⁻⁹ /h		≤ 2 × 10 ⁻⁹ /h	

Note

1. The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature is not exceeded.

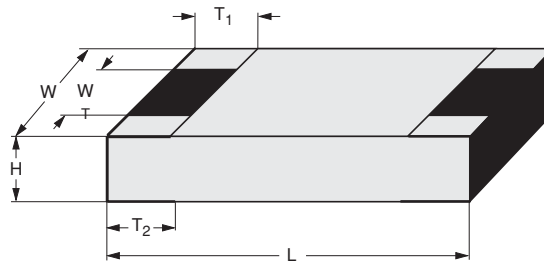


ORDERING INFORMATION - type description and ordering code							
M	C	T	0603	-50	1 %	P5	47 K
FILM TYPE	PRODUCT CODE	SIZE CODE	IMPERIAL SIZE	TEMPERATURE COEFFICIENT	TOLERANCE	PACKAGING	RESISTANCE VALUE
M = Metal film or cermet	C = Flat chip	S = 0402 T = 0603 U = 0805	0402 0603 0805	± 25 ppm/K ± 50 ppm/K	± 1 % ± 0.5 %	P5 = 5 000 units, 4mm pitch ⁽¹⁾ E0 = 10 000 units, 2 mm pitch ⁽²⁾ PW = 20 000 units, 4 mm pitch ⁽¹⁾	See Temperature coefficient and resistance range table

Note

1. P5 and PW only for MCT 0603 and MCU 0805.
2. E0 only for MCS 0402.
3. Jumpers are ordered by the resistance value 0 Ω , e.g. MCT 0603 P5 0R0.

DIMENSIONS



DIMENSIONS - chip resistor types, mass and relevant physical dimensions							
TYPE	H (mm)	L (mm)	W (mm)	W _T (mm)	T ₁ (mm)	T ₂ (mm)	MASS (mg)
MCS 0402	0.32 ± 0.05	1.0 ± 0.05	0.5 ± 0.05	> 75 % of W	$0.2 + 0.1/-0.15$	0.2 ± 0.1	0.6
MCT 0603	$0.45 + 0.1/-0.05$	1.55 ± 0.05	0.85 ± 0.1	> 75 % of W	$0.3 + 0.15/-0.2$	$0.3 + 0.15/-0.2$	1.9
MCU 0805	$0.45 + 0.1/-0.05$	2.0 ± 0.1	1.25 ± 0.15	> 75 % of W	$0.4 + 0.1/-0.2$	$0.4 + 0.1/-0.2$	4.6

TEMPERATURE COEFFICIENT AND RESISTANCE RANGE				
DESCRIPTION		RESISTANCE VALUE ⁽¹⁾		
T.C.	TOLERANCE	MCS 0402	MCT 0603	MCU 0805
± 50 ppm/K	± 1 %	10 Ω to 4.99 MΩ	1 Ω to 10 MΩ	-
	± 0.5 %	100 Ω to 221k Ω	39 Ω to 511 k Ω	10 Ω to 1.5 M Ω
± 25 ppm/K	± 0.5 %	100 Ω to 221 kΩ	39 Ω to 511 kΩ	10 Ω to 1.5 M Ω
Jumper	-	≤ 20 mΩ; I_{max} = 0.63 A	≤ 20 mΩ; I_{max} = 1 A	≤ 20 m Ω ; I _{max} = 1.5 A

Note

1. Resistance values to be selected for ± 1 % tolerance from E24 and E96; for ± 0.5 % tolerance from E24 and E192.

Resistance ranges printed in bold are preferred T.C. / tolerance combinations with optimized availability.



DESCRIPTION

Production is strictly controlled and follows an extensive set of instructions established for reproducibility. A homogeneous film of metal alloy is deposited on a super high grade (96 % Al_2O_3) ceramic substrate and conditioned to achieve the desired temperature coefficient. Specially designed inner contacts are deposited on both sides. A special laser is used to achieve the target value by smoothly cutting a meander groove in the resistive layer without damaging the ceramics. For the high ohmic range, optimized Cermet products provide comparable properties. The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The terminations receive a final pure tin on nickel plating.

The result of the determined production is verified by an extensive testing procedure and optical inspection performed on 100 % of the individual chip resistors. Only accepted products are laid directly into the paper tape in accordance with **EN 60286-3**.

ASSEMBLY

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using wave, reflow or vapour phase. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions. The resistors are lead (Pb)-free, the pure tin plating provides compatibility with lead (Pb)-free and lead-containing soldering processes. The immunity of the plating against tin whisker growth has been proven under extensive testing. All products comply with the CEFIC-EECA-EICTA list of legal restrictions on hazardous substances.

This includes full compatibility with the following directives:

- 2000/53/EC End of Vehicle life Directive (ELV)
- 2000/53/EC Annex II to End of Vehicle Life Directive (ELV II)
- 2002/95/EC Restriction of the use of Hazardous Substances Directive (RoHS)
- 2002/96/EC Waste Electrical and Electronic Equipment Directive (WEEE)

Solderability is specified for 2 years after production or re-qualification. The permitted storage time is 20 years.

APPROVALS

The resistors are tested in accordance with **EN 140401-801** (superseding **CECC 40401-801**) which refers to **EN 60115-1** and **EN 140400**. Approval of conformity is indicated by the **CECC** logo on the package label.

Vishay BEYSCHLAG has achieved "**Approval of Manufacturer**" in accordance with **EN 100114-1**. The release certificate for "**Technology Approval Schedule**" in accordance with **CECC 240 001** based on **EN 100114-6** is granted for the Vishay BEYSCHLAG manufacturing process.

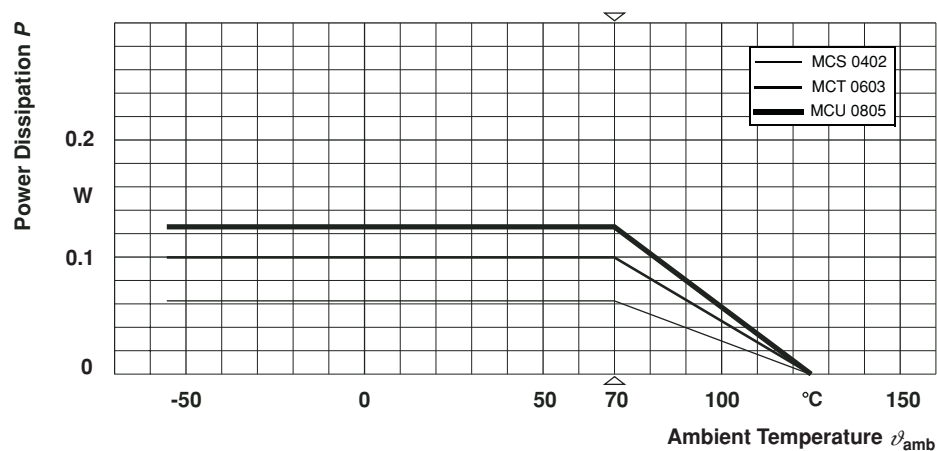
SPECIALS

This product family of thin film flat chip resistors is completed by **Zero Ohm Jumpers**.

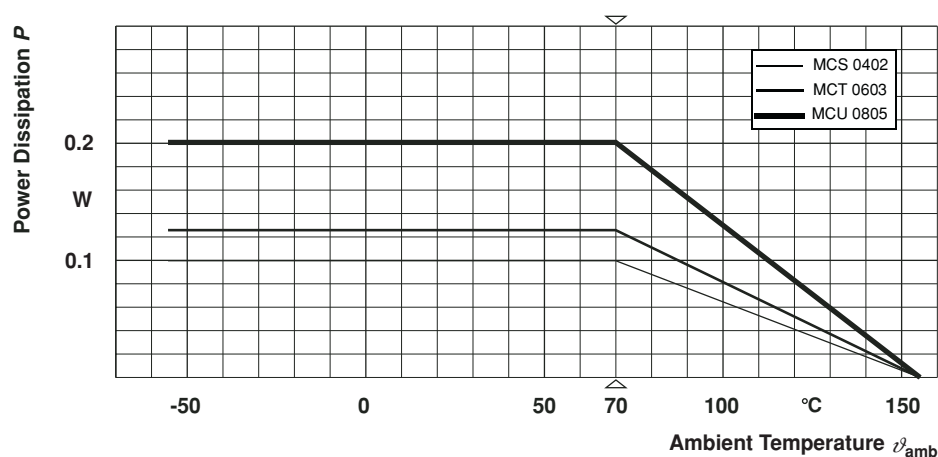
On request, resistors are available with established reliability in accordance with **EN 140401-801 Version E**. Please refer to the special data sheet for information on failure rate level, available resistance ranges and order codes.



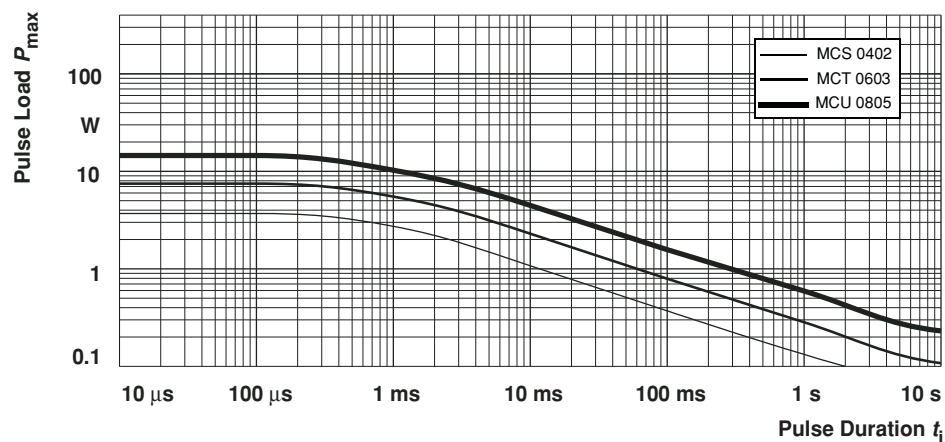
FUNCTIONAL PERFORMANCE



Derating - Standard Operation

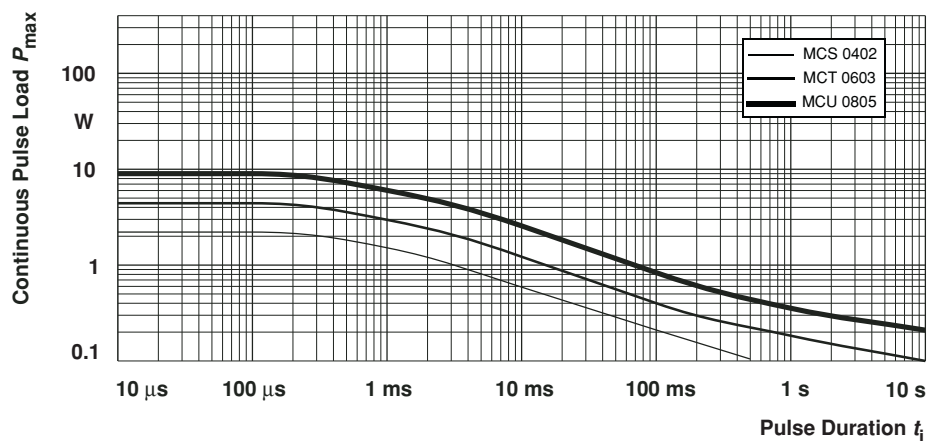


Derating - Power Operation



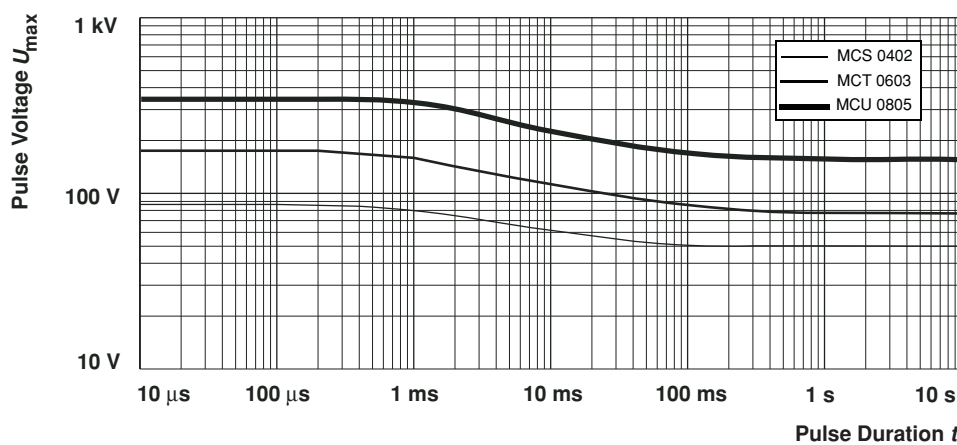
Maximum pulse load, single pulse; for permissible resistance change equivalent to 8 000 h operation.

Single Pulse



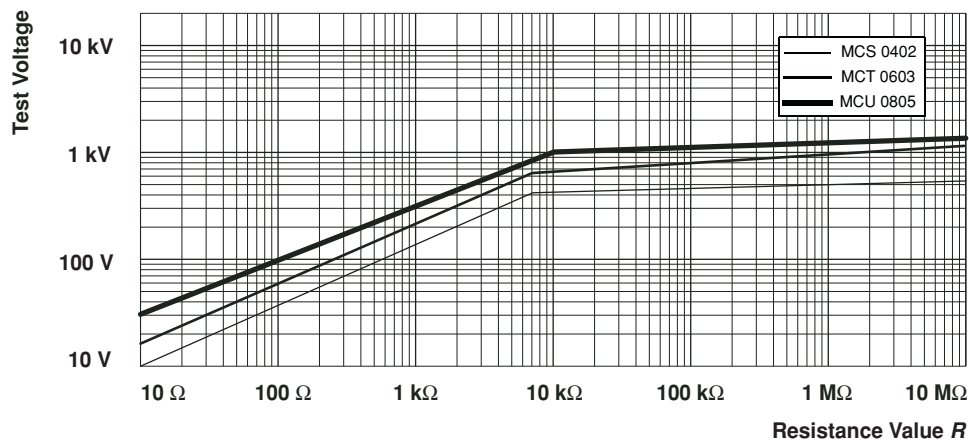
Maximum pulse load, continuous pulses; for permissible resistance change equivalent to 8 000 h operation.

Continuous Pulse



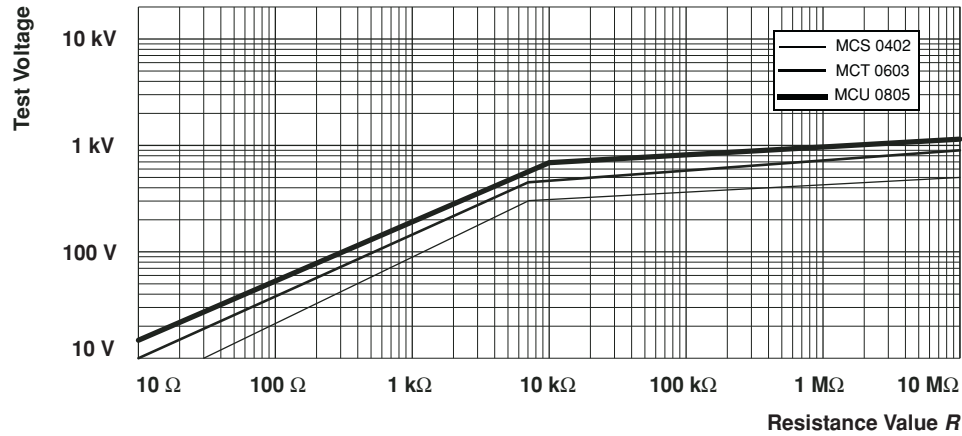
Maximum pulse voltage, single and continuous pulses; for permissible resistance change equivalent to 8 000 h operation.

Pulse Voltage



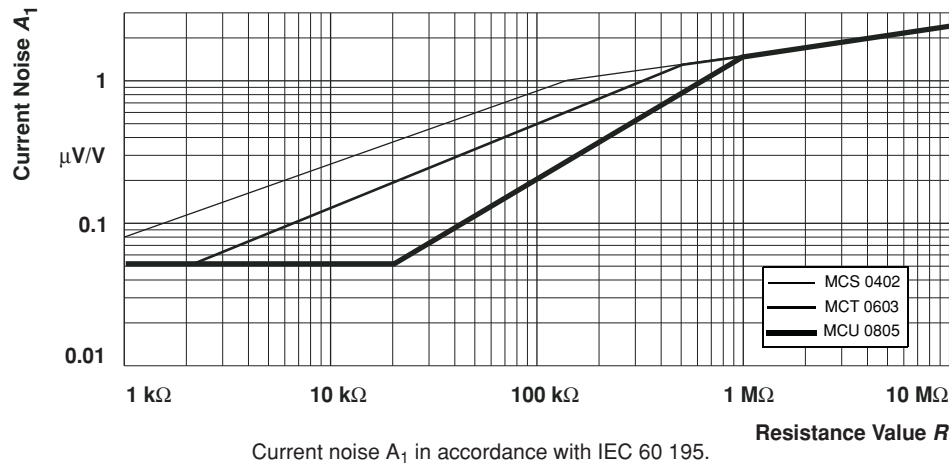
Pulse load rating in accordance with EN 60115-1 clause 4.27; 1.2 μ s / 50 μ s; 5 pulses at 12 s interval; for permissible resistance change 0.5 %.

1.2/50 Pulse



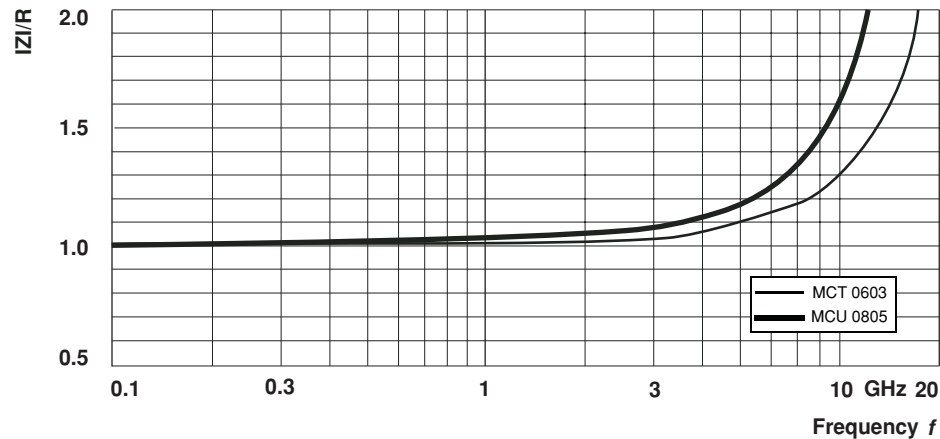
Pulse load rating in accordance with EN 60115-1 clause 4.27; 10 μ s / 700 μ s;
10 pulses at 1 minute intervals; for permissible resistance change 0.5 %.

10/700 Pulse



Current noise A_1 in accordance with IEC 60 195.

Current Noise



$|Z|/R$ for 49.9 Ω chip resistor.

RF-Behaviour

TESTS AND REQUIREMENTS

All tests are carried out in accordance with the following specifications:

EN 60115-1, Generic specification (includes tests)

EN 140 400, Sectional specification (includes schedule for qualification approval)

EN 140 401-801, Detail specification (includes schedule for conformance inspection)

The components are approved in accordance with the European CECC-system, where applicable. The following table contains only the most important tests. For the full test schedule refer to the documents listed above. The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5202.

The tests are carried out in accordance with IEC 60068 and under standard atmospheric conditions in accordance with IEC 60068-1, 5.3. Climatic category LCT/UCT/56 (rated

temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days) is valid.

Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C

Relative humidity: 45 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1 060 mbar).

The components are mounted for testing on boards in accordance with EN 60115-1, 4.31 unless otherwise specified.

The requirements stated in the Test Procedures and Requirements table are based on the required tests and permitted limits of EN 140 401-801. However, some additional tests and a number of improvements against those minimum requirements have been included.

TEST PROCEDURES AND REQUIREMENTS					
EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE ($\Delta R/R$)	
				STABILITY CLASS 0.5	STABILITY CLASS 1
			stability for product types:	10 Ω to 33.2 kΩ	> 33.2 kΩ to 4.99 MΩ
			MCS 0402		
			MCT 0603		
			MCU 0805		
4.5	–	resistance		± 1 %; ±0.5 %	
4.8.4.2	–	temperature coefficient	at 20 / –55 / 20 °C and 20 / 125 / 20 °C	± 50 ppm/K; ±25 ppm/K	
4.25.1	–	endurance at 70 °C: standard operation mode	$U = \sqrt{P_{70} \times R}$ or $U = U_{\max}$; whichever is the less severe; 1.5 h on; 0,5 h off 70 °C; 1000 h 70 °C; 8000 h	±(0.25 % R + 0.05 Ω) ±(0.5 % R + 0.05 Ω)	
		endurance at 70 °C: power operation mode	$U = \sqrt{P_{70} \times R}$ or $U = U_{\max}$; whichever is the less severe; 1,5 h on; 0,5 h off 1.5 h on; 0,5 h off 70 °C; 1000 h 70 °C; 8000 h	± (0.5 % R + 0.05 Ω) ± (1 % R + 0.05 Ω)	



TEST PROCEDURES AND REQUIREMENTS - continued

EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE ($\Delta R/R$)	
				STABILITY CLASS 0.5	STABILITY CLASS 1
			stability for product types:		
			MCS 0402	10 Ω to 33.2 kΩ	> 33.2 kΩ to 4.99 MΩ
			MCT 0603	10 Ω to 100 kΩ	1 Ω to < 10 Ω; > 100 kΩ to 10 MΩ
			MCU 0805	10 Ω to 221 kΩ	> 221 kΩ to 10 MΩ
4.25.3	–	endurance at upper category temperature	125 °C; 1 000 h 155 °C; 1 000 h	± (0.25 % <i>R</i> + 0.05 Ω) ± (0.5 % <i>R</i> + 0.05 Ω)	± (0.5 % <i>R</i> + 0.05 Ω) ± (1 % <i>R</i> + 0.05 Ω)
4.24	78 (Cab)	damp heat, steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (0.5 % <i>R</i> + 0.05 Ω)	± (1 % <i>R</i> + 0.05 Ω)
4.23 4.23.2 4.23.3 4.23.4 4.23.5 4.23.6	2 (Ba) 30 (Db) 1 (Aa) 13 (M) 30 (Db)	climatic sequence: dry heat damp heat, cyclic cold low air pressure damp heat, cyclic	UCT; 16 h 55 °C; 24 h; > 90 % RH; 1 cycle LCT; 2 h 8.5 kPa; 2 h; 25 ± 10 °C 55 °C; 5 days; > 90 % RH; 5 cycles LCT = – 55 °C; UCT = 125 °C	± (0.5 % <i>R</i> + 0.05 Ω)	± (1 % <i>R</i> + 0.05 Ω)
–	1 (Aa)	cold	– 55 °C; 2 h	± (0.1 % <i>R</i> + 0.01 Ω)	± (0.25 % <i>R</i> + 0.05 Ω)
4.19	14 (Na)	rapid change of temperature	30 minutes at LCT and 30 minutes at UCT; LCT = – 55 °C; UCT = 125 °C; 5 cycles	± (0.1 % <i>R</i> + 0.01 Ω) no visible damage	
			LCT = – 55 °C; UCT = 125 °C; 1000 cycles	± (0.25 % <i>R</i> + 0.05 Ω) no visible damage	
4.13	–	short time overload; standard operation mode	$U = 2.5 \times \sqrt{P_{70} \times R}$ or $U = 2 \times U_{\max}$; whichever is the less severe; 5 s	± (0.1 % <i>R</i> + 0.01 Ω)	± (0.25 % <i>R</i> + 0.05 Ω)
		short time overload; power operation mode		± (0.25 % <i>R</i> + 0.05 Ω)	± (0.5 % <i>R</i> + 0.05 Ω)
4.27	–	single pulse high voltage overload; standard operation mode	severity no. 4: $U = 10 \times \sqrt{P_{70} \times R}$ or $U = 2 \times U_{\max}$; whichever is the less severe; 10 pulses 10 μs/700 μs	± (0.5 % <i>R</i> + 0.05 Ω) no visible damage	

TEST PROCEDURES AND REQUIREMENTS - continued

EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE ($\Delta R/R$)	
				STABILITY CLASS 0.5	STABILITY CLASS 1
			stability for product types:		
			MCS 0402	10 Ω to 33.2 kΩ	> 33.2 kΩ to 4.99 MΩ
			MCT 0603	10 Ω to 100 kΩ	1 Ω to < 10 Ω; > 100 kΩ to 10 MΩ
			MCU 0805	10 Ω to 221 kΩ	> 221 kΩ to 10 MΩ
4.37	–	periodic electric overload; standard operation mode	$U = \sqrt{15 \times P_{70} \times \bar{R}}$ or $U = 2 \times U_{\max}$; whichever is the less severe; 0.1 s on; 2.5 s off; 1000 cycles	$\pm(0.5 \% \bar{R} + 0.05 \Omega)$ no visible damage	
		periodic electric overload; power operation mode		$\pm(1 \% \bar{R} + 0.05 \Omega)$ no visible damage	
4.22	6 (Fc)	vibration	endurance by sweeping; 10 to 2000 Hz; no resonance; amplitude ≤ 1.5 mm or ≤ 200 m/s ² ; 6 h	$\pm(0.1 \% \bar{R} + 0.01 \Omega)$ no visible damage	
4.17.2	58 (Td)	solderability	solder bath method; SnPb40; non-activated flux (215 $\pm$ 3) °C; (3 $\pm$ 0.3) s	good tinning (≥ 95 % covered); no visible damage	
			solder bath method; SnAg3Cu0,5 or SnAg3,5; non-activated flux; (235 $\pm$ 3) °C; (2 $\pm$ 0.2) s	good tinning (≥ 95 % covered); no visible damage	
4.18.2	58 (Td)	resistance to soldering heat	solder bath method; (260 $\pm$ 5) °C; (10 $\pm$ 1) s	$\pm(0.1 \% \bar{R} + 0.01 \Omega)$ no visible damage	$\pm(0.25 \% \bar{R} + 0.05 \Omega)$ no visible damage
4.29	45 (XA)	component solvent resistance	isopropyl alcohol + 50 °C; method 2	no visible damage	
4.32	21 (Ue ₃)	shear (adhesion)	RR 1005M and RR 1608 M; 9 N	no visible damage	
			RR 2012M; 45 N		
4.33	21 (Ue ₁)	substrate bending	depth 2 mm, 3 times	$\pm(0.1 \% \bar{R} + 0.01 \Omega)$ no visible damage; no open circuit in bent position	
4.7	–	voltage proof	$U_{\text{rms}} = U_{\text{Ins}}$; 60 $\pm$ 5 s	no flashover or breakdown	
4.35	–	flammability	IEC 60695-2-2, needle flame test; 10 s	no burning after 30 s	



ORDERING INFORMATION

Components may be ordered by using either a simple clear text ordering code, see "Type description and ordering code" or Vishay BCcomponents' unique 12NC.

Numeric Ordering Code (12NC)

- The resistors have a 12-digit ordering code starting with 2312.
- The subsequent 4 digits indicate the resistor type, specification and packaging; see the 12NC Ordering Code table.
- The remaining 4 digits indicate the resistance value:
 - The first 3 digits indicate the resistance value.
 - The last digit indicates the resistance decade in accordance with the Last digit of 12NC Indicating Resistance Decade table.

Last Digit of 12NC Indicating Resistance Decade

RESISTANCE DECADE	LAST DIGIT
1 Ω to 9.99 Ω	8
10 Ω to 99.9 Ω	9
100 Ω to 999 Ω	1
1 k Ω to 9.99 k Ω	2
10 k Ω to 99.9 k Ω	3
100 k Ω to 999 k Ω	4
1 M Ω to 9.99 M Ω	5
10 M Ω to 99.9 M Ω	6

Ordering example

The ordering code of a MCT 0603 resistor, value 47 k Ω and TC 50 with $\pm 1\%$ tolerance, supplied in cardboard tape of 5 000 units per reel is: 2312 215 14703.

12NC ORDERING CODE - resistor type and packaging

DESCRIPTION			ORDERING CODE 2312		
			CARDBOARD TAPE ON REEL		
TYPE	T.C.	TOL.	P5 (5000 UNITS)	E0 (10000 UNITS)	PW (20000 UNITS)
MCS 0402	± 50 ppm/K	$\pm 1\%$	–	275 1....	–
		$\pm 0.5\%$	–	275 5....	–
	± 25 ppm/K	$\pm 0.5\%$	–	276 5....	–
	jumper	–	–	275 90001	–
MCT 0603	± 50 ppm/K	$\pm 1\%$	215 1....	–	205 1....
		$\pm 0.5\%$	215 5....	–	205 5....
	± 25 ppm/K	$\pm 0.5\%$	216 5....	–	206 5....
	jumper	–	215 90001	–	205 90001
MCU 0805	± 50 ppm/K	$\pm 0.5\%$	255 5....	–	245 5....
	± 25 ppm/K	$\pm 0.5\%$	256 5....	–	246 5....
	jumper	–	255 90001	–	245 90001

Resistance ranges printed in bold are preferred T.C. / tolerance combinations with optimized availability.