

VCXO

VG-4030JA

Product number (please contact us)

Q3612JA0xxxxx00

- Frequency range : 2 MHz to 28.63636 MHz
- Operating voltage : 3.3 V
- Pull range : 150×10^{-6} or 180×10^{-6} / $V_C=0.0$ to 3.0 V
- Thickness : 4.7 mm Max.
- Lead(Pb)-free : Comply with EU RoHS directive



Actual size

■ Specifications (characteristics)

Item		Symbol	Specifications	Remarks
			DVK	
Output frequency range		f_0	2.0000 MHz to 28.63636 MHz	Please contact us for inquiries about the available frequency.
Power source voltage	Max. supply voltage	V_{DD-GND}	-0.5 V to +7.0 V	
	Operating voltage	V_{DD}	3.3 V $\pm$ 0.17 V	
Temperature range	Storage temperature	T_{STG}	-55 °C to +125 °C	Stored as bare product after unpacking
	Operating temperature	T_{OPR}	-20 °C to +70 °C	
Frequency stability		$\Delta f/f_0$	$\pm 37 \times 10^{-6}$ Max.	$V_C=1.8$ V
Current consumption		I_{OP}	18 mA Max.	No load condition
Pull range		Δf_c	150×10^{-6} or 180×10^{-6}	$V_C=0.0$ to 3.0 V (Please contact us.)
Input resistance		Z_{IN}	10 M Ω Min.	DC Level
Frequency change polarity			Positive polarity	$V_C=0.0$ to 3.0 V
Duty		tw/t	40 % to 60 %	CMOS load: 50 % V_{DD} level
High output voltage		V_{OH}	$V_{DD}-0.4$ V Min.	$I_{OH}=-0.8$ mA
Low output voltage		V_{OL}	0.4 V Max.	$I_{OL}=3.2$ mA
Output load condition		C_L	30 pF Max.	CMOS load
Output rise fall time *		t_R / t_F	6 ns Max.	CMOS load: 20 % V_{DD} level to 80 % V_{DD} level
Oscillation start up time		t_{OSC}	10 ms Max.	Time at minimum operating voltage to be 0 s
Aging		f_a	$\pm 5 \times 10^{-6}$ / year Max.	$T_a=+25$ °C, $V_{DD}=3.3$ V, First year

*Give the V_C voltage at the time of the power supply input as GND or Open without fail.

■ External dimensions

(Unit:mm) ■ Recommended soldering pattern (Unit:mm)

