Preferred Devices

Plastic Medium Power Silicon NPN Transistor

Designed for use in 5.0 to 10 W audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

• DC Current Gain -

 $h_{FE} = 40 \text{ (Min) } @ I_C = 0.15 \text{ Adc}$

• Epoxy Meets UL94, VO @ 1/8"

• ESD Ratings: Human Body Model, 3B; >8000 V Machine Model, C; >400 V

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	100	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector Current	I _C	2.0	Adc
Base Current	Ι _Β	1.0	Adc
Total Device Dissipation @ T _C = 25°C	P_{D}	25	Watts
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5.0	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



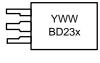
ON Semiconductor®

http://onsemi.com

2.0 AMPERES POWER TRANSISTORS NPN SILICON 80 VOLTS 25 WATTS







CASE 77-09 TO-225

x = 7 or 8
 Y = Year
 WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping		
BD237	TO-225	500 Units/Box		
BD238	TO-225	500 Units/Box		

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage* (I _C = 0.1 Adc, I _B = 0)	V _{(BR)CEO}	80	-	Vdc
Collector Cutoff Current (V _{CB} = 100 Vdc, I _E = 0)	I _{CBO}	-	0.1	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	1.0	mAdc
DC Current Gain $(I_C = 0.15 \text{ A}, V_{CE} = 2.0 \text{ V})$ $(I_C = 1.0 \text{ A}, V_{CE} = 2.0 \text{ V})$	h _{FE1} h _{FE2}	40 25	- -	-
Collector–Emitter Saturation Voltage* (I _C = 1.0 Adc, I _B = 0.1 Adc)	V _{CE(sat)}	-	0.6	Vdc
Base–Emitter On Voltage* (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	-	1.3	Vdc
Current–Gain – Bandwidth Product (I _C = 250 mAdc, V _{CE} = 10 Vdc, f = 1.0 MHz)	f _T	3.0	-	MHz

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

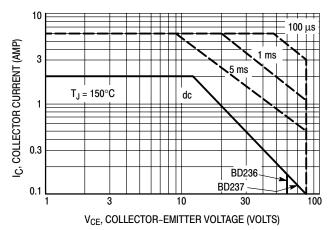


Figure 1. Active Region Safe Operating Area

The Safe Operating Area Curves indicate $I_{C-}V_{CE}$ limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum T_J , power—temperature derating must be observed for both steady state and pulse power conditions.

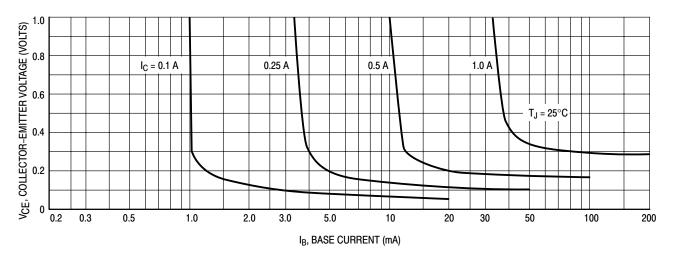
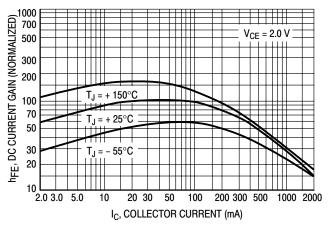


Figure 2. Collector Saturation Region



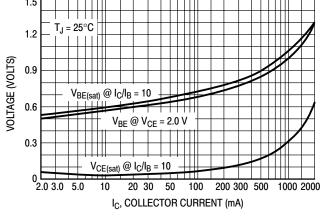


Figure 3. Current Gain

Figure 4. "On" Voltages

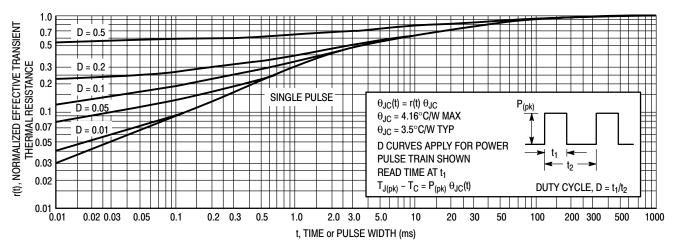
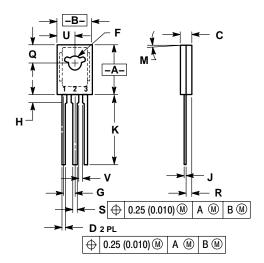


Figure 5. Thermal Response

PACKAGE DIMENSIONS

TO-225 CASE 77-09 ISSUE Z



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- 14-3M, 1982. CONTROLLING DIMENSION: INCH. 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
С	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094	BSC	2.39 BSC	
Н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
M	5° TYP		5° TYP	
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
V	0.040		1.02	

STYLE 1:

PIN 1. EMITTER COLLECTOR 3. BASE

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