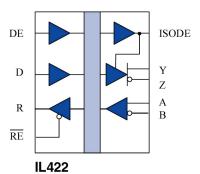


Isolated RS422/RS485 Interface

Functional Diagram



V _{ID1} (Y-Z)	V _{ID2} (A-B)	DE	RE	ISODE	R	D	Mode
X	$\geq 200 \; mV$	X	L	X	Н	X	Receive
X	≤ -200 mV	X	L	X	L	X	Receive
-7 <v<sub>ID<12</v<sub>	-7 <v<sub>ID<12</v<sub>	X	Н	X	Z	X	Receive/Drive
≥1.5 V	≥1.5 V	Н	L	Н	Н	Н	Drive
≤-1.5 V	≤-1.5 V	Н	L	Н	L	L	Drive
	Open	L	L	L	Н	X	Receive

H = High Level, L = Low Level X = Irrelevant, Z = High Impedance

Features

- +3.3 V Input Supply Compatible
- 2500 V_{rms} Isolation (1 min.)
- 25 ns Maximum Propagation Delay
- 25 Mbps Data Rate
- 1 ns Pulse Skew (typ.)
- ±60 mA Driver Output Capability
- Thermal Shutdown Protection
- Meets or Exceeds ANSI 422-B, EIA 485-A and ITU Recommended V11
- -40°C to +85°C Temperature Range
- PROFIBUS International Component Recognition
- 16-pin SOIC Package
- UL1577 Approval Pending
- IEC 61010-1 Approval Pending

Applications

Multi-point or multi-drop transmission on long bus lines in noisy environments.

Description

The IL422 is a galvanically isolated, high-speed differential driver and receiver pair, designed for bidirectional data communication on balanced transmission lines. Isolation is achieved through patented* Isoloop® technology. The IL422 is the first isolated RS422 interface in a standard 16-pin SOIC package that meets the ANSI Standards EIA/TIA-422-B and RS485.

The IL422 has current limiting and thermal shutdown features to protect against output short circuits and bus contention situations where these may cause excessive power dissipation.



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REV. G



Absolute Maximum Ratings(11)

Parameters	Symbol	Min.	Тур.	Max.	Units	Test Conditions
Storage Temperature	T_{s}	-65		150	°C	
Ambient Operating Temperature	T_{A}	-40		85	°C	
Voltage Range at A or B Bus Pins		-7		12	V	
Supply Voltage (1)	V_{DD1}, V_{DD2}	-0.5		7	V	
Digital Input Voltage		-0.5		5.5	V	
Digital Output Voltage		-0.5		$V_{DD}+1$	V	
Continuous Total Power Dissipation				725 377	mW	25°C 85°C
Maximum Output Current	I_{o}			95	mA	
Lead Solder Temperature				260	°C	10 sec.
ESD			2		kV	HBM

Recommended Operating Conditions

Parameters	Symbol	Min.	Typ.	Max.	Units	Test Conditions
Supply Voltage	$egin{array}{c} V_{ m DD1} \ V_{ m DD2} \end{array}$	3.0 4.5		5.5 5.5	V	
Input Voltage at any Bus Terminal	V_{I}	1.5		12	V	
(separately or common mode)	V_{IC}			-7		
High-Level Digital Input Voltage	$V_{\scriptscriptstyle IH}$	2.4 3.0			V	$V_{DD1} = 3.3 \text{ V} $ $V_{DD1} = 5.0 \text{ V}$
Low-Level Digital Input Voltage	$V_{\scriptscriptstyle \mathrm{IL}}$			0.8	V	
Differential Input Voltage (2)	$V_{\text{\tiny ID}}$			±12	V	
High-Level Output Current (Driver)	I_{OH}			-60	mA	
High-Level Digital Output Current (Receiver)	I_{OH}			8	mA	
Low-Level Output Current (Driver)	I_{OL}			60	mA	
Low-Level Digital Output Current (Receiver)	I_{OL}			8	mA	
Ambient Operating Temperature	T _A	-40		85	°C	
Digital Input Signal Rise and Fall Times	$t_{\rm IR}, t_{\rm IF}$	DC Stable				

Insulation Specifications

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Parameters	Symbol	Min.	Тур.	Max.	Units	Test Conditions
Creepage Distance		8.077				mm
Barrier Impedance			>10 ¹⁴ 7			Ω pF
Leakage Current			0.2		μA	$240 \text{ V}_{\text{RMS}}, 60 \text{ Hz}$

Safety & Approvals

IEC61010-1

TUV Certificate Numbers: Approval Pending

Classification: Reinforced Insulation

Model	Package	Pollution Degree	Material Group	Max. Working Voltage
IL485	0.3" SOIC	II	III	$300~\mathrm{V_{RMS}}$

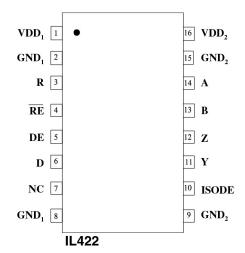
UL 1577

Component Recognition program File #: Approval Pending Rated $2500V_{RMS}$ for 1 minute (SOIC)



IL422 Pin Connections

<u>:= :== :</u>		
1	V_{DD1}	Input Power Supply
2	GND_1	Input Power Supply Ground Return
3	R	Output Data from Bus
4	RE	Read Data Enable (if \overline{RE} is high, R= high impedance)
5	DE	Drive Enable
6	D	Data Input to Bus
7	NC	No Internal Connection
8	GND_1	Input Power Supply Ground Return.
9	GND_2	Output Power Supply Ground Return.
10	ISODE	Isolated DE Output for use in Profibus applications where the state of the isolated drive enable node needs to be monitored.
11	Y	Y Bus (Drive – True)
12	Z	Z Bus (Drive – Inverse)
13	В	B Bus (Receive – Inverse)
14	A	A Bus (Receive – True)
15	GND ₂	Output Power Supply Ground Return.
16	V_{DD2}	Output Power Supply





Driver Section

Electrical specifications are T_{min} to T_{max} unless otherwise stated.

Parameters	Symbol	Min.	Typ. ⁽⁵⁾	Max.	Units	Test Conditions
Input Clamp Voltage	V_{IK}			-1.5	V	$I_{L} = -18 \text{ mA}$
Output voltage	V_{o}	0		6	V	$I_0 = 0$
Differential Output Voltage ⁽²⁾	$ V_{OD1} $	1.5		6	V	$I_O = 0$
Differential Output Voltage ⁽²⁾	$ V_{OD2} $	1.5	2.5	5	V	$R_L = 54 \Omega$, $V_{DD} = 5 V$
Differential Output Voltage ⁽²⁾⁽⁶⁾	V_{OD3}	1.5		5	V	$R_L = 54 \Omega, V_{DD} = 4.5 V$
Change in Magnitude of Differential Output Voltage ⁽⁷⁾	$\Delta V_{ m OD} $			±0.2	V	$R_L = 54 \Omega \text{ or } 100 \Omega$
Common Mode Output Voltage	V_{oc}			3 -1	V	$R_L = 54 \Omega \text{ or } 100 \Omega$
Change in Magnitude of Common Mode Output Voltage ⁽⁷⁾	$\Delta V_{ m oc} $			±0.2	V	$R_L = 54 \Omega \text{ or } 100 \Omega$
Output Current ⁽⁴⁾ Output Disabled	I_{o}			1 -0.8	mA	$V_{o} = 12V$ $V_{o} = -7V$
High Level Input Current	$I_{ m IH}$			10	μΑ	$V_{\rm I} = 3.5 \text{ V}$
Low Level Input Current	$I_{\scriptscriptstyle \mathrm{IL}}$			-10	μΑ	$V_{I} = 0.4 \text{ V}$
				250		$V_0 = -6 \text{ V}$
Short-circuit Output Current	I_{os}	60		-150	mA	$V_0 = 0 V$
				-250		$V_0 = 8 V$
Supply Current $(V_{DD2} = +5 V)$	I_{DD2}		27	34		No Load
$(V_{DD1} = +5 V)$	I_{DD1}		5	10	mA	(Outputs Enabled)
$(V_{DD1} = +3.3 \text{ V})$	I_{DD1}		3.2	7		(**************************************
D		Switching Spe	cifications (5)	3.5	** **	Toronto and
Parameters	Symbol	Min.	Typ. ⁽⁵⁾	Max.	Units	Test Conditions
Maximum Data Rate	(2-)	25			Mbps	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Differential Output Prop Delay	$t_D(OD)$		16	25	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Pulse Skew ⁽¹⁰⁾	$t_s(P)$		1	6	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Differential Output Rise & Fall Time	$t_{T}(OD)$		8	10	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Enable Time to High Level	t_{PZH}		31	65	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Enable Time to Low Level	$t_{ m PZL}$		22	35	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Disable Time from High Level	t_{PHZ}		28	50	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Disable Time from Low Level	$t_{\scriptscriptstyle PLZ}$		16	32	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Skew Limit ⁽³⁾	$t_{SK}(LIM)$		2	8	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$

Notes: (These apply to both driver and receiver sections)

- 1. All voltage values are with respect to network ground except differential I/O bus voltages.
- 2. Differential input/output voltage is measured at the noninverting terminal A with respect to the inverting terminal B.
- 3. Skew limit is the maximum propagation delay difference between any two devices at 25°C.
- 4. The power-off measurement in ANSI Standard EIA/TIA-422-B applies to disabled outputs only and is not applied to combined inputs and outputs.
- 5. All typical values are at V_{DD1} , $V_{DD2} = 5$ V or $V_{DD1} = 3.3$ V and $T_A = 25$ °C.
- 6. The minimum V_{OD2} with a 100 Ω load is either ½ V_{OD1} or 2 V, whichever is greater.
- 7. $\Delta |V_{OD}|$ and $\Delta |V_{OC}|$ are the changes in magnitude of V_{OD} and V_{OC} , respectively, that occur when the input is changed form one logic state to the other.
- 8. This applies for both power on and power off, refer to ANSI standard RS-485 for exact condition. The EIA/TIA-422-B limit does not apply for a combined driver and receiver terminal.
- 9. Includes 8 ns read enable time. Maximum propagation delay is 25 ns after read assertion.
- 10. Pulse skew is defined as the $|t_{PLH}$ $t_{PHL}|$ of each channel.



Receiver Section

Electrical specifications are T	T_{min} to T_{max} u	ınless otherwise stated.
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Parameters	Symbol	Min.	Typ. ⁽⁵⁾	Max.	Units	Test Conditions
Positive-going Input Threshold Voltage	V_{IT^+}			0.2	V	$V_0 = 2.7 \text{ V},$ $I_0 = -0.4 \text{ mA}$
Negative-going Input Threshold Voltage	V _{IT} -	-0.2			V	$V_0 = 0.5 \text{ V},$ $I_0 = 8 \text{ mA}$
Hysteresis Voltage (V _{IT+} - V _{IT-})	$V_{\scriptscriptstyle \mathrm{HYS}}$		60		mV	
High Level Digital Output Voltage	$ m V_{OH}$	V _{DD} - 0.2			V	$V_{ID} = 200 \text{ mV}$ $I_{OH} = -20 \mu\text{A}$
Low Level Digital Output Voltage	$V_{\scriptscriptstyle OL}$			0.2	V	$V_{ID} = -200 \text{ mV}$ $I_{OH} = 20 \mu\text{A}$
High-impedance-state output current	I_{OZ}			±10	μΑ	$V_0 = 0.4 \text{ to } (V_{DD2} - 0.5) \text{ V}$
Line Input Current ⁽⁸⁾	$I_{\scriptscriptstyle \rm I}$			1 -0.8	mA	$V_{I} = 12 V$ $V_{I} = -7 V$ $Other Input^{(11)} = 0 V$
Input Resistance	$r_{\rm I}$		50		kΩ	
Supply Current $(V_{DD2} = +5V)$ $(V_{DD1} = +5V)$ $(V_{DD1} = +3.3V)$	$egin{array}{l} { m I_{DD2}} \\ { m I_{DD1}} \\ { m I_{DD1}} \end{array}$		27 5 3.2	34 10 7	mA	No load Outputs Enabled
(* DDI - 3.3 *)		tching Charact				
Parameters	Symbol	Min.	Typ. ⁽⁵⁾	Max.	Units	Test Conditions
Maximum Data Rate	,	25			Mbps	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Propagation Delay ⁽⁹⁾	$t_{\scriptscriptstyle PD}$		24	32	ns	$V_0 = -1.5 \text{ V to } 1.5 \text{ V},$ $C_L = 15 \text{ pF}$
Pulse Skew ⁽¹⁰⁾	t _{sk} (P)		1	6	ns	$V_0 = -1.5 \text{ V to } 1.5 \text{ V},$ $C_L = 15 \text{ pF}$
Skew Limit ⁽³⁾	$t_{SK}(LIM)$		2	8	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Enable Time To High Level	$t_{ m PZH}$		17	24	ns	$C_L = 15 \text{ pF}$
Output Enable Time To Low Level	$t_{\scriptscriptstyle PZL}$		30	45	ns	$C_L = 15 \text{ pF}$
Output Disable Time From High Level	$t_{ ext{PHZ}}$		30	45	ns	$C_L = 15 \text{ pF}$
Output Disable Time From Low Level	$t_{\scriptscriptstyle PLZ}$		18	27	ns	$C_L = 15 \text{ pF}$
		ching Characte	ristics @ 3.3 V		T	
Parameters	Symbol	Min.	Typ. ⁽⁵⁾	Max.	Units	Test Conditions
Maximum Data Rate		25			Mbps	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Propagation Delay ⁽⁹⁾	t_{PD}		27	32	ns	$V_0 = -1.5 \text{ V to } 1.5 \text{ V},$ $C_L = 15 \text{ pF}$
Pulse Skew ⁽¹⁰⁾	$t_{SK}(P)$		2	6	ns	$V_0 = -1.5 \text{ V to } 1.5 \text{ V},$ $C_L = 15 \text{ pF}$
Skew Limit ⁽³⁾	$t_{SK}(LIM)$		4	8	ns	$R_L = 54 \Omega, C_L = 50 \text{ pF}$
Output Enable Time To High Level	$t_{\scriptscriptstyle \mathrm{PZH}}$		20	24	ns	$C_L = 15 \text{ pF}$
Output Enable Time To Low Level	$t_{\scriptscriptstyle \mathrm{PZL}}$		33	45	ns	$C_L = 15 \text{ pF}$
Output Disable Time From High Level	$t_{\scriptscriptstyle PHZ}$		33	45	ns	$C_L = 15 \text{ pF}$
Output Disable Time From Low Level	$t_{\scriptscriptstyle{\mathrm{PLZ}}}$		20	27	ns	$C_L = 15 \text{ pF}$

Electrostatic Discharge Sensitivity

This product has been tested for electrostatic sensitivity to the limits stated in the specifications. However, NVE recommends that all integrated circuits be handled with appropriate care to avoid damage. Damage caused by inappropriate handling or storage could range from performance degradation to complete failure.



Application Information

Power Consumption

IsoLoop devices achieve their low power consumption from the manner by which they transmit data across the isolation barrier. By detecting the edge transitions of the input logic signal and converting these to narrow current pulses, a magnetic field is created around the GMR Wheatstone bridge. Depending on the direction of the magnetic field, the bridge causes the output comparator to switch following the input logic signal. Since the current pulses are narrow, about 2.5 ns wide, the power consumption is independent of mark-to-space ratio and solely dependent on frequency. This has obvious advantages over optocouplers whose power consumption is heavily dependent on its on state and frequency.

The approximate power supply current per channel is:

$$I_{\rm IN} = 40~x~\frac{f}{f_{\rm MAX}}~x~\frac{1}{4}~mA$$

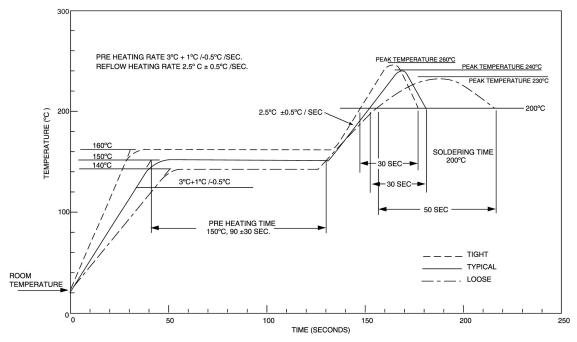
Where f = operating frequency

$$f_{MAX} = 50 \text{ MHz}$$

Power Supplies

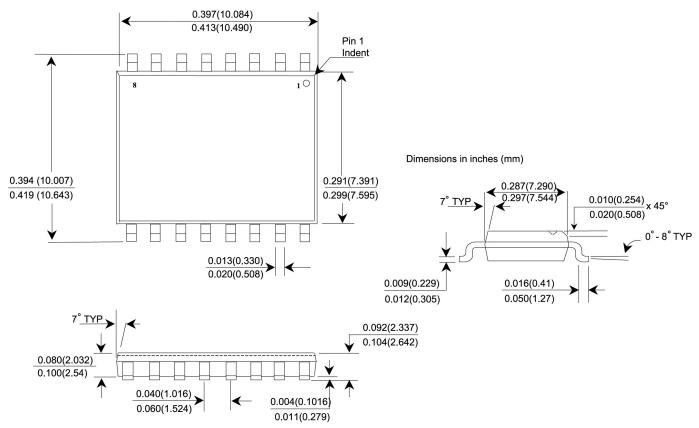
Low ESR capacitors such as ceramic are required to decouple the supplies. Both V_{DD1} and V_{DD2} must be bypassed with 47 nF capacitors. These should be placed as close as possible to V_{DD} pins for proper operation. In addition, $V_{\,DD2}$ should have a 10 μF tantalum capacitor connected in parallel with the 47 nF capacitor.

IR Soldering Profile

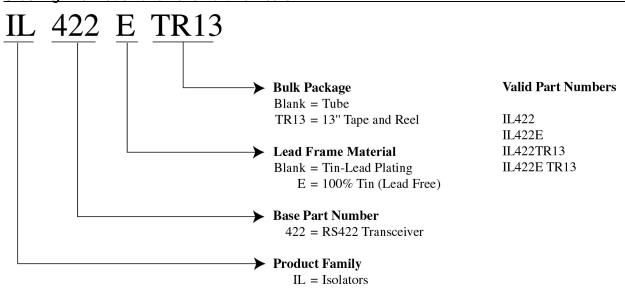




0.3" 16-pin SOIC Package



Ordering Information and Valid Part Numbers





ISB-DS-001-IL422-G

Changes

- 1. Page 1: Revision letter added.
- 2. Page 1: Ordering Information Removed.
- 3. Page 2: IEC 61010-1 Classification: "Reinforced Insulation" added.
- 4. Page 4: Notes added.
- 5. Page 6: IR Soldering Profile added
- 6. Page 7: Ordering Information added.



About NVE

An ISO 9001 Certified Company

NVE Corporation is a high technology components manufacturer having the unique capability to combine spintronic Giant Magnetoresistive (GMR) materials with integrated circuits to make high performance electronic components. Products include Magnetic Field Sensors, Magnetic Field Gradient Sensors (Gradiometer), Digital Magnetic Field Sensors, Digital Signal Isolators and Isolated Bus Transceivers.

NVE is a leader in GMR research and in 1994 introduced the world's first products using GMR material, a line of GMR magnetic field sensors that can be used for position, magnetic media, wheel speed and current sensing.

NVE is located in Eden Prairie, Minnesota, a suburb of Minneapolis. Please visit our Web site at www.nve.com or call (952) 829-9217 for information on products, sales or distribution.

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ISB-DS-001-IL422-G

September 30, 2005