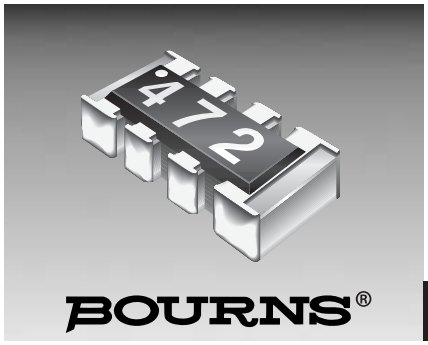


BOURNS®

Features

- Improvement of placement efficiency
- 2/4 isolated resistors in an 0404/0804 size package
- Convex termination style
- E24 series from 10 ohms to 1 megohm
- Resistance tolerance ±5 %
- Suitable for all types of soldering processes
- Paper tape on reel for automatic placement

Model CAY10 - Chip Resistor Array

Specifications

Requirement	Characteristics	Test Method
Short Time Overload	±2 %	Rated Voltage X 2.5, 5 seconds
Soldering Heat	±1 %	260 °C ±5 °C, 10 seconds ±1 second
Temperature Cycling (5)	±1 %	125 °C (30 minutes) - normal (15 minutes) -30 °C (30 minutes) - normal (15 minutes)
Moisture Load Life	±3 %	1000 hours
Load Life	±3 %	1000 hours

Characteristics

Characteristics	CAY10-xxxJ2	CAY10-xxxJ4
Number of Elements (Isolated)	2	4
Power Rating @ 70 °C per Element	31mW	
Package Power Rating @ 70 °C	63 mW	125 mW
Resistance Tolerance	±5 %	
Resistance Range (E24)	10 ohms - 1 megohm	
T.C.R.	±250 ppm/°C	
Max. Overload Voltage	50V	
Max. Working Voltage	25V	
Operating Temp. Range	-55 °C to +125 °C	
Rating Temperature	+70 °C	
Packaging	10,000 pieces per reel	

How To Order

CA Y 10 - 103 J 4 __

Chip Arrays

Type
- Y = Convex

Model
- 10 = 04 Package Width

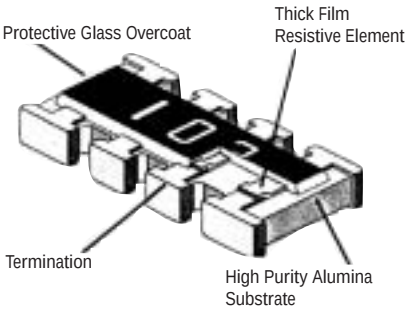
Resistance Code
- 103 = 10 K ohms

Resistance Tolerance
- J = ±5 %

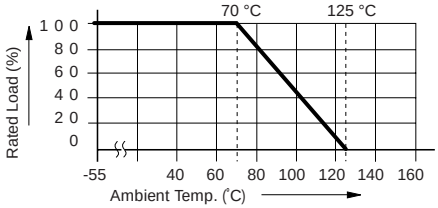
Resistors
- 2 = 2 Resistors
- 4 = 4 Resistors

Terminations
- LF = Tin-plated
- Blank = Solder-plated

Construction

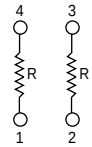


Derating Curve

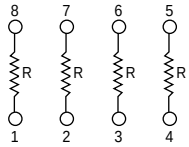


Isolated Circuit

CAY10-xxxJ2



CAY10-xxxJ4



Model CAY10 - Chip Resistor Array

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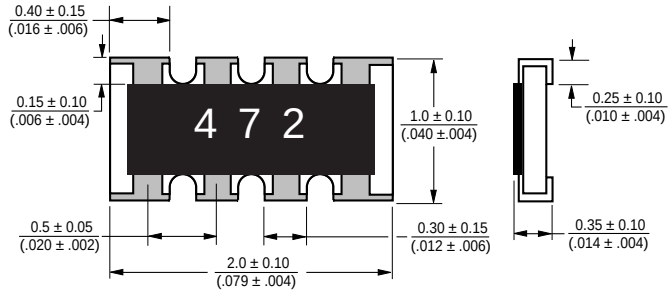
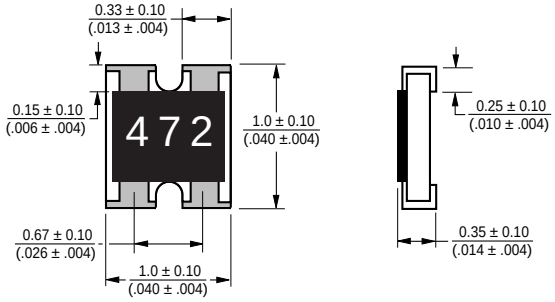
Product Dimensions

CAY10-xxxJ4

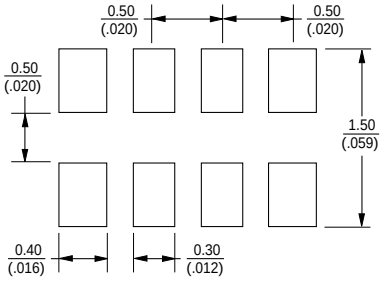


CAY10-xxxJ2



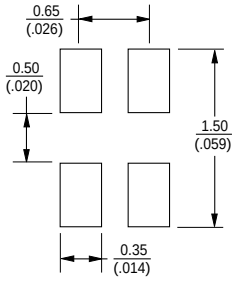
Land Pattern

CAY10-xxxJ4



DIMENSIONS: MM / (INCHES)

CAY10-xxxJ2



Packaging Dimensions