SN54ALS645A, SN54AS645, SN74ALS645A, SN74AS645 OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

SDAS278 - JANUARY 1995

- Bidirectional Bus Transceivers in High-Density 20-Pin Packages
- True Logic
- 3-State Outputs
- Package Options Include Plastic Small-Outline (DW) Packages, Ceramic Chip Carriers (FK), and Standard Plastic (N) and Ceramic (J) 300-mil DIPs

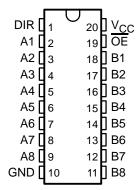
description

These octal bus transceivers are designed for asynchronous two-way communication between data buses. These devices transmit data from the A bus to the B bus or from the B bus to the A bus, depending on the level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can be used to disable the device so that the buses are effectively isolated.

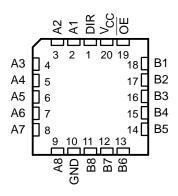
The -1 version of the SN74ALS645A is identical to the standard version, except that the recommended maximum I_{OL} is increased to 48 mA. There is no -1 version of the SN54ALS645A.

The SN54ALS645A and SN54AS645 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS645A and SN74AS645 are characterized for operation from 0°C to 70°C.

SN54ALS645A, SN54AS645 . . . J PACKAGE SN74ALS645A, SN74AS645 . . . DW OR N PACKAGE (TOP VIEW)



SN54ALS645A, SN54AS645 . . . FK PACKAGE (TOP VIEW)



FUNCTION TABLE

INP	UTS	ODEDATION				
OE	DIR	OPERATION				
L	L	B data to A bus				
L	Н	A data to B bus				
Н	Χ	Isolation				

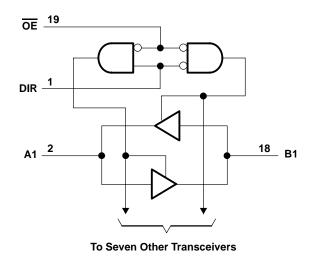
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logic symbol†

ŌĒ DIR 3 EN1 [BA] 3 EN2 [AB] 18 В1 \triangleright 2 ▽ 17 B2 16 А3 **B3** 15 Α4 В4 14 Α5 **B5** 13 **B6** A6 12 Α7 **B7** 11 Α8 В8

logic diagram (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

	7 V
Input voltage, V _I : All inputs	7 V
I/O ports	5.5 V
Operating free-air temperature range, TA: SN54ALS	6645A –55°C to 125°C
SN74ALS	6645A 0°C to 70°C
Storage temperature range	–65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SNS	54ALS64	5A	SN7	4ALS64	5A	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			8.0	V
ІОН	High-level output current			-12			-15	mA
la.	Low lovel output ourrent			12			24	A
lOL	Low-level output current						48§	mA
TA	Operating free-air temperature	-55		125	0		70	°C

[§] Applies only to the -1 version and only if V_{CC} is between 4.75 V and 5.25 V



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		SNS	4ALS64	5A	SN74ALS645A			UNIT	
	PARAMETER	TEST CON	TEST CONDITIONS		TYP†	MAX	MIN	TYP	MAX	UNII	
٧ıĸ		$V_{CC} = 4.5 \text{ V},$	I _I = -18 mA			-1.5			-1.5	V	
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -0.4 \text{ mA}$	V _{CC} -2	2		V _{CC} -2	2			
\ _{\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\}			$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V	
VOH		V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2						V	
			$I_{OH} = -15 \text{ mA}$				2				
			I _{OL} = 12 mA		0.25	0.4		0.25	0.4		
VOL		V _{CC} = 4.5 V	I _{OL} = 24 mA					0.35	0.5	V	
			$I_{OL} = 48 \text{ mA}^{\ddagger}$					0.35	0.5		
Ī.	Control inputs	V _{CC} = 5.5 V	V _I = 7 V			0.1			0.1	mA	
11	A or B ports		V _I = 5.5 V			0.1			0.1	IIIA	
Ī	Control inputs	$V_{CC} = 5.5 \text{ V},$ $V_{I} = 2\overline{Z}Y' \text{ V}$				20			20		
ΊΗ	A or B ports§				20			20	μΑ		
	Control inputs	V00 - 5 5 V	V₁ =ʊ:¤′ ∨			-0.1			-0.1	mA	
IIL	A or B ports \S $V_{CC} = 5.5 \text{ V},$		V = 0:4 V	-0.1		-0.1			-0.1	IIIA	
Io¶		V _{CC} = 5.5 V,	V _O = 2.25 V	-20		-112	-30		-112	mA	
	_		Outputs high		30	48		30	45	mA	
ICC		V _{CC} = 5.5 V	Outputs low		36	60		36	55		
			Outputs disabled		38	63		38	58		

switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C _L R1 R2	_ = 50 pF l = 500	2,	,	UNIT	
			SI		SN54ALS645A		SN74ALS645A	
			MIN	MAX	MIN	MAX		
t _{PLH}	A or B	D A	1	19	3	10	ns	
t _{PHL}		B or A	1	14	3	10	110	
^t PZH	ŌĒ	A B		30	5	20	ns	
t _{PZL}	OE	A or B	2	29	5	20	115	
^t PHZ	ŌĒ	A or B	2	14	2	10	ns	
^t PLZ	OE .	AUIB	2	30	4	15	110	

[#] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.



[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ Applies only to the -1 version and only if V_{CC} is between 4.75 V and 5.25 V § For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

SN54ALS645A, SN54AS645, SN74ALS645A, SN74AS645 **OCTAL BUS TRANSCEIVERS** WITH 3-STATE OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	7 V
Input voltage, V _I : All inputs	7 V
I/O ports	
Operating free-air temperature range, T _A : SN54AS645	55°C to 125°C
SN74AS645	0°C to 70°C
Storage temperature range	_65°C to 150°C

recommended operating conditions

		SI	N54AS64	15	SN74AS645		UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH}	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			0.8			0.8	V
IOH	High-level output current			-12			-15	mA
lOL	Low-level output current			48			64	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEOT 001	TEST CONDITIONS		154AS64	15	SN			
		lesi coi			TYP [‡]	MAX	MIN	TYP [‡]	MAX	UNIT
٧ıK		V _{CC} = 4.5 V,	I _I = -18 mA			-1.2			-1.2	V
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V},$	$I_{OH} = -2 \text{ mA}$	V _{CC} -2	?		V _{CC} -2			
\/a			$I_{OH} = -3 \text{ mA}$	2.4	3.2		2.4	3.2		V
VOH		V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2.4						V
			$I_{OH} = -15 \text{ mA}$				2.4			
Val		V _{CC} = 4.5 V	$I_{OL} = 48 \text{ mA}$		0.3	0.55				V
VOL			$I_{OL} = 64 \text{ mA}$					0.35	0.55	
١.	Control inputs	V _{CC} = 5.5 V	V _I = 7 V			0.1			0.1	mA
11	A or B ports		V _I = 5.5 V			0.1			0.1	IIIA
i	Control inputs	V 55V	$V_{CC} = 5.5 \text{ V},$ $V_{I} = 27.7 \text{ V}$			20			20	μА
ΊΗ	A or B ports§	$V_{CC} = 5.5 \text{ V},$	V = 2.7 V			70			70	μΑ
l	Control inputs	V _{CC} = 5.5 V,	VI =∿'.;丼' ∨			-0.5			-0.5	mA
IIΓ	A or B ports§	VCC = 5.5 v,	V = 0.4 V		-0.75				-0.75	ША
IOI		$V_{CC} = 5.5 V,$	V _O = 2.25 V	-50		-150	-50		-150	mA
			Outputs high		62	97		62	97	
ICC		V _{CC} = 5.5 V	Outputs low		95	149		95	149	mA
			Outputs disabled		79	123		79	123	

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

[§] For I/O ports, the parameters I_{IH} and I_{IL} include the off-state output current.

The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, IOS.

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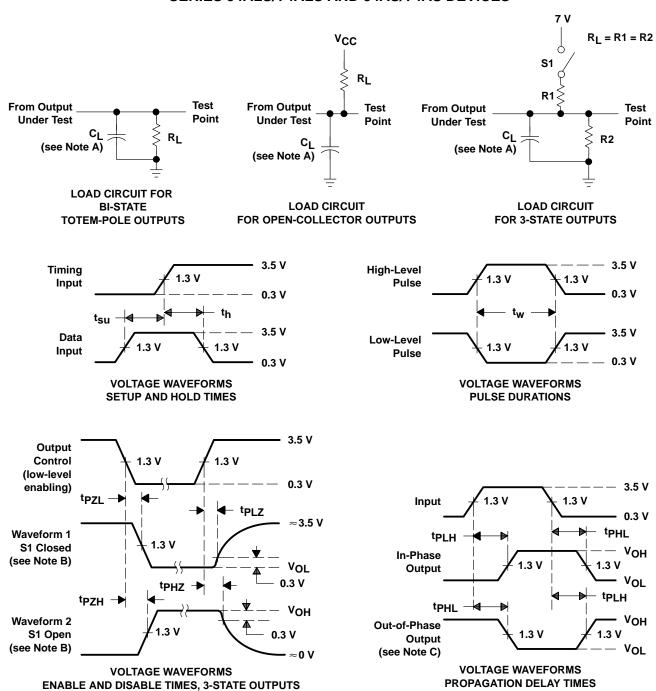
switching characteristics (see Figure 1)

PARAMETER	FROM (INPUT)	_		= 50 pF = 500 c = 500 c	2,	V,	UNIT
			SN54A	SN54AS645		SN74AS645	
			MIN	MAX	MIN	MAX	
^t PLH	A or B		2	11	2	9.5	
^t PHL		B or A	2	10.5	2	9	ns
^t PZH	ŌĒ	A D	2	12	2	11	20
^t PZL	OE	A or B	2	12	2	10	ns
^t PHZ	ŌĒ	A or B	2	8	2	7	nc
t _{PLZ}	OE .	AUID	2	13	2	12	ns

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

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PARAMETER MEASUREMENT INFORMATION SERIES 54ALS/74ALS AND 54AS/74AS DEVICES



NOTES: A. C_I includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. When measuring propagation delay items of 3-state outputs, switch S1 is open.
- All input pulses have the following characteristics: PRR \leq 1 MHz, $t_{\Gamma} = t_{f} = 2$ ns, duty cycle = 50%.
- The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuits and Voltage Waveforms



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